SCHEMATIC DIAGRAM

MODEL : 27WL56P

WARNING : BEFORE SERVICING THIS CHASSIS, READ THE "SERVICE SAFETY PRECAUTIONS" ON THE MANUAL FOR THIS MODEL.

CAUTION : The international hazard symbols "⚠" in the schematic diagram and the parts list designate components which have special characteristics important for safety and should be replaced only with types identical to those in the original circuit or specified in the parts list. The mounting position of replacements is to be identical with originals. Before replacing any of these components, read carefully the SERVICE SAFETY PRECAUTIONS on the MANUAL for this model. Do not degrade the safety of the receiver through improper servicing.

NOTE:

1. RESISTOR Resistance is shown in ohm [K = 1.000, M = 1.000.000]. All resistors are 1/6W and 5% tolerance carbon resistor, unless otherwise noted as the following marks.
   1/2R = Metal or Metal oxide of 1/2 watt 1/2S = Carbon compsistion of 1/2 watt
   1RF = Fuse resistor of 1 watt 10W = Cement of 10 watt
   K = ±10% G = ±2% F = ±1%
2. CAPACITOR Unless otherwise noted in schematic, all capacitor values less than 1 are expressed in μF, and the values more than 1 in pF.
   All capacitors are ceramic 50V, unless otherwise noted as the following marks.
   —|(+)|— Electolytic capacitor —|(M)|— Mylar capacitor
3. The parts indicated with " ⚠ " have special characteristics, and should be replaced with identical parts only.
4. Voltages read with DIGITAL MULTI-METER from point indicated to chassing ground, using a color bar signal with all controls at normal, line voltage 220 volts.
5. Waveforms are taken receiving color bar signal with enough sensitivity.
6. Voltage reading shown are nominal values and may vary ±20% except H.V.

■ SCHEMATIC DIAGRAM STRUCTURE:

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AV_JACK 2/48
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AUDIO_OUT 4/48
REGULATOR 5/48
KEY 6/48
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PC-IN_BOARD 8/48
CONNECTOR(POWER_BOARD) 9/48
MAIN_POWER(POWER_BOARD) 10/48
SUB_POWER(POWER_BOARD) 11/48
AC-INPUT 12/48

SIGNAL
- ANALOG IN [SHEET-200] 13/48
- D IN A [SHEET-300] 14/48
- D IN B [SHEET-301] 15/48
- HDMI (Link, I2C) [SHEET-302] 16/48
- HDMI Rx#2 (Video/Audio) [SHEET-303] 17/48
- HDMI Rx#3 (PWR,Audio) [SHEET-304] 18/48
- HDMI AUDIO PLL [SHEET-305] 19/48
- HDMI AUDIO DAC [SHEET-306] 20/48
- HDMI AUDIO OUT [SHEET-307] 21/48
- HDMI CONTROLLER [SHEET-308] 22/48
- EEPROM1 [SHEET-309] 23/48
- EEPROM2 [SHEET-310] 24/48
- MICRO I/O [SHEET-400] 25/48
- OCM MEMORY I/F [SHEET-401] 26/48
- FLASH MEMORY [SHEET-402] 27/48
- SYNC SEPA [SHEET-403] 28/48
- E2P OTHER [SHEET-404] 29/48
- STD-BY MICOR [SHEET-405] 30/48
- CORTEZ REG 1 [SHEET-406] 31/48
- CORTEZ REG 2 [SHEET-407] 32/48
- CORTEZ REG 3 [SHEET-408] 33/48
- BOOT CONFIG [SHEET-409] 34/48
- SERVICE CONNECTOR [SHEET-410] 35/48
- I2C Switch [SHEET-411] 36/48
- AUDIO
  - (1/2) [SHEET-600] 37/48
  - (2/2) [SHEET-601] 38/48
- CORTEZ 1 [SHEET-700] 39/48
- CORTEZ 2 [SHEET-701] 40/48
- DDR I/F [SHEET-702] 41/48
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- DCDC CONV. [SHEET-802] 44/48
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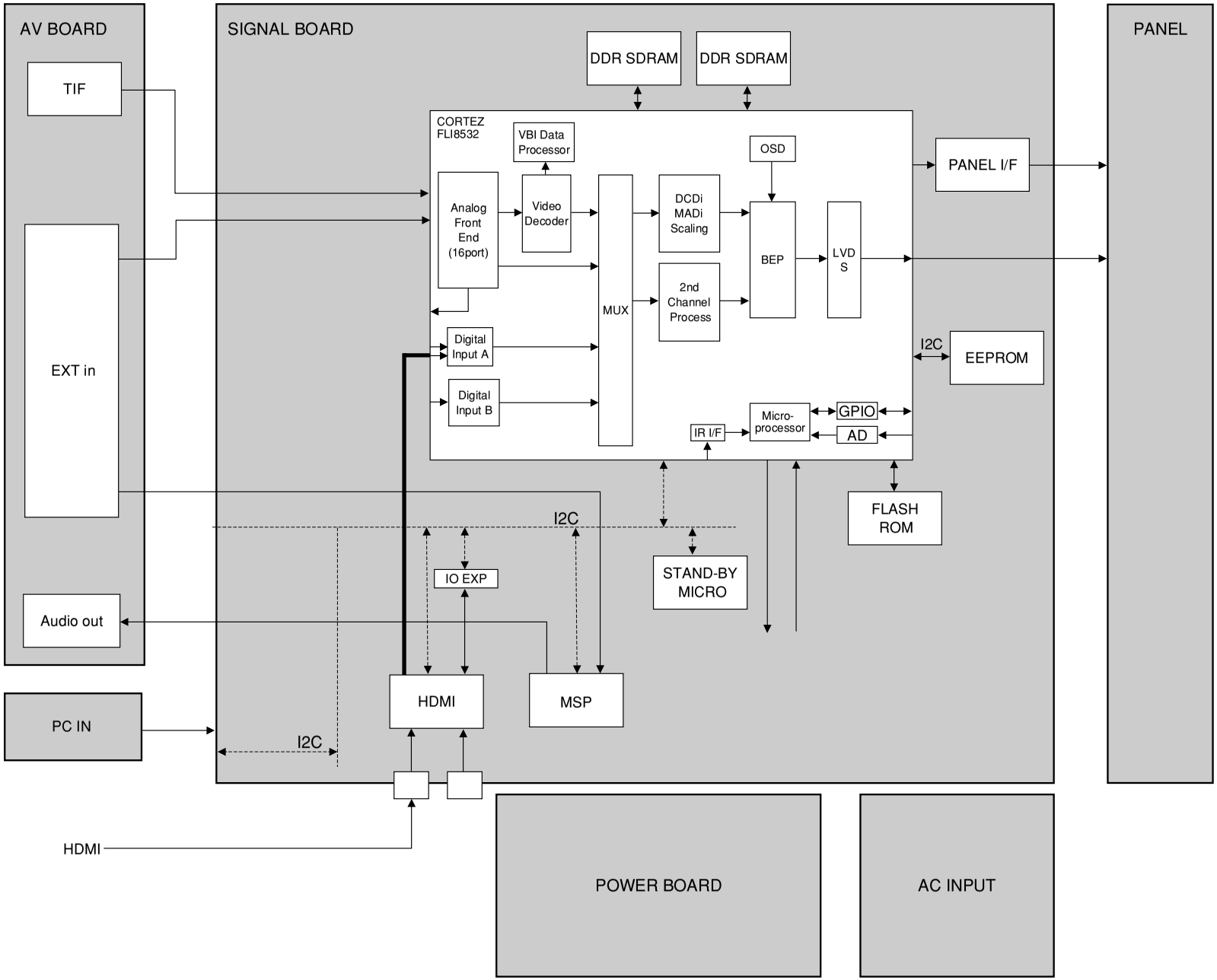
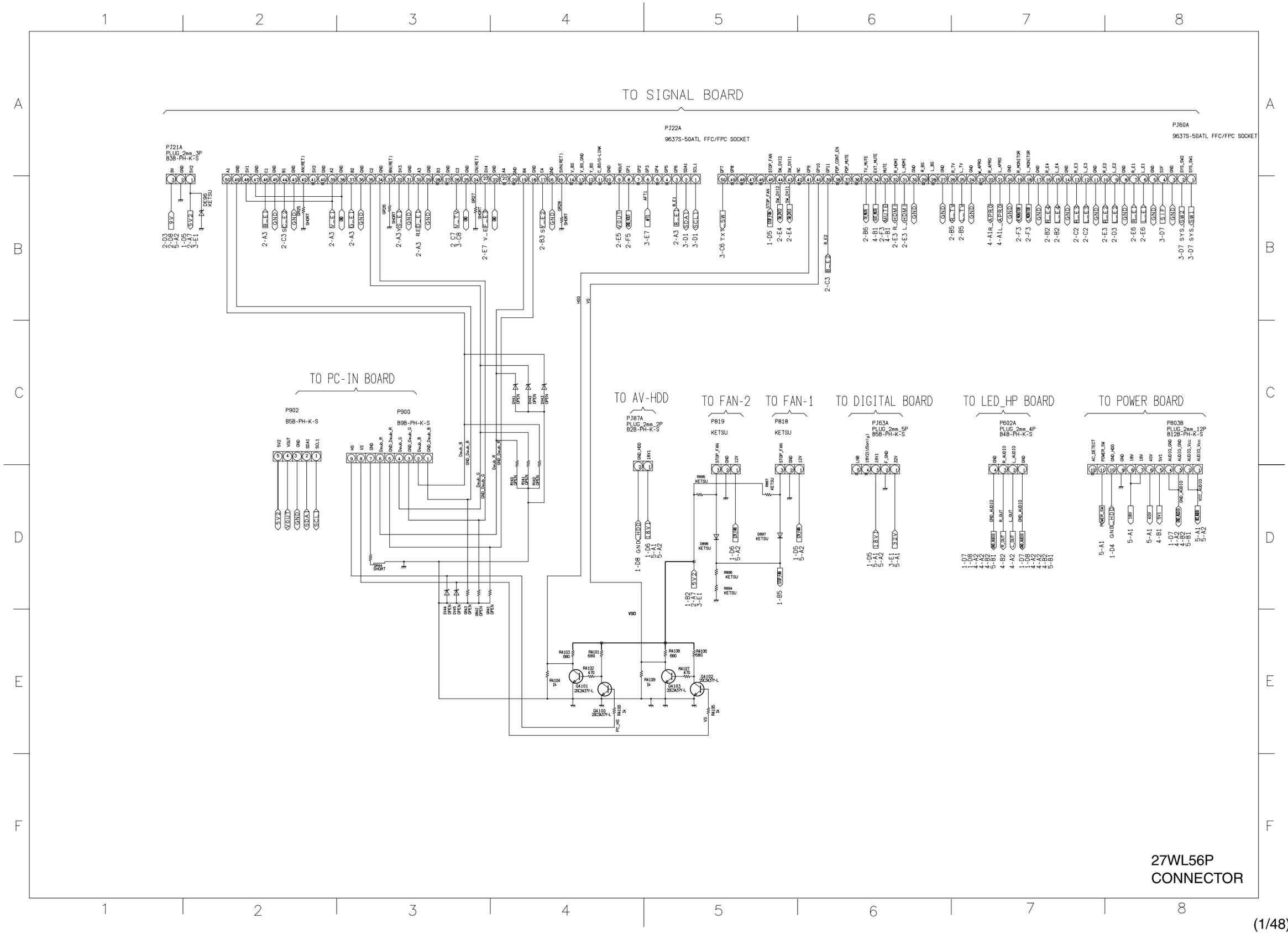
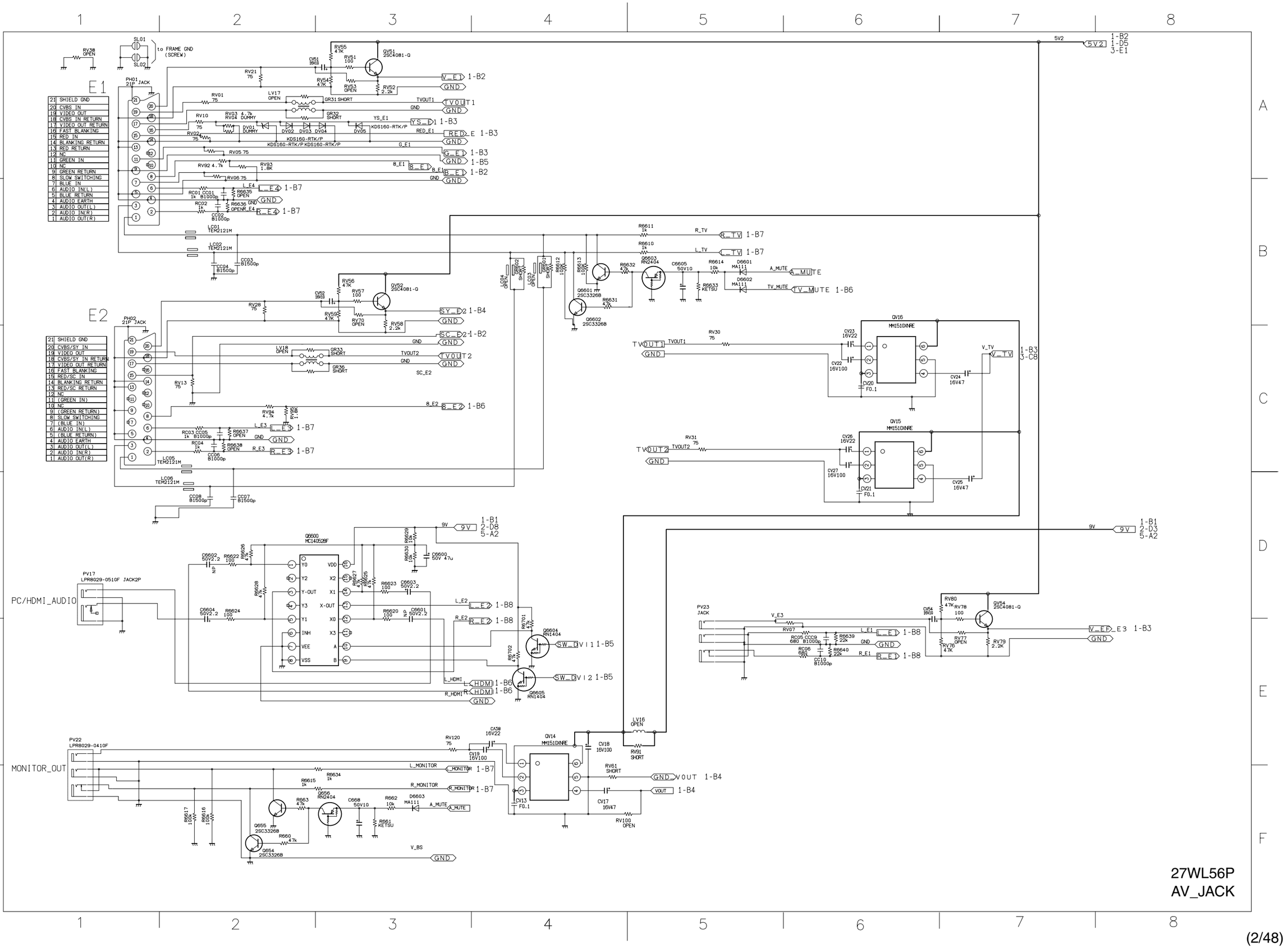
- Power Connector and Dimming [SHEET-902] 47/48
- LVDS Power and Others [SHEET-903] 48/48

CIRCUIT BLOCK DIAGRAM




TO SIGNAL BOARD
PJ22A
9637S-50ATL FFC/FPC SOCKET
PJ60A
9637S-50ATL FFC/FPC SOCKET
PJ21A
PLUG_2mm_3P
B3B-PH-K-S
TO PC-IN BOARD
P902
B5B-PH-K-S
P900
B9B-PH-K-S
TO AV-HDD
PJ87A
PLUG_2mm_2P
B2B-PH-K-S
TO FAN-2
P819
KETSU
TO FAN-1
P818
KETSU
TO DIGITAL BOARD
PJ63A
PLUG_2mm_5P
B5B-PH-K-S
TO LED_HP BOARD
P602A
PLUG_2mm_4P
B4B-PH-K-S
TO POWER BOARD
P803B
PLUG_2mm_12P
B12B-PH-K-S
27WL56P
CONNECTOR

(1/48)


E1
E2
PH01 21P JACK
PH02 21P JACK
SL01
SL02
to FRAME GND (SCREW)
21 SHIELD GND
20 CVBS_IN
19 VIDEO OUT
18 CVBS_IN RETURN
17 VIDEO OUT RETURN
16 FAST BLANKING
15 RED IN
14 BLANKING RETURN
13 RED RETURN
12 NC
11 GREEN IN
10 NC
9 GREEN RETURN
8 SLOW SWITCHING
7 BLUE IN
6 AUDIO IN(L)
5 BLUE RETURN
4 AUDIO EARTH
3 AUDIO OUT(L)
2 AUDIO IN(R)
1 AUDIO OUT(R)
21 SHIELD GND
20 CVBS/SY IN
19 VIDEO OUT
18 CVBS/SY IN RETURN
17 VIDEO OUT RETURN
16 FAST BLANKING
15 RED/SC IN
14 BLANKING RETURN
13 RED/SC RETURN
12 NC
11 (GREEN IN)
10 NC
9 (GREEN RETURN)
8 SLOW SWITCHING
7 (BLUE IN)
6 AUDIO IN(L)
5 (BLUE RETURN)
4 AUDIO EARTH
3 AUDIO IN(R)
2 AUDIO OUT(R)
5V2
V_E1
TVOUT1
YS_E1
RED_E1
G_E1
8_E1
L_E4
R_E4
L_TV
R_TV
A_MUTE
TV_MUTE
SY_E2
SC_E2
TVOUT2
L_E3
R_E3
8_E2
L_HDMI
R_HDMI
L_E2
R_E2
V_E3
L_E1
R_E1
L_MONITOR
R_MONITOR
V_BS
9V
PC/HDMI_AUDIO
MONITOR_OUT
Q6600 MC14052BF
QV51 2SC4081-Q
QV52 2SC4081-Q
QV53 2SC4081-Q
QV54 2SC4081-Q
QV14 MM1510XNRE
QV15 MM1510XNRE
QV16 MM1510XNRE
Q6601 2SC3326B
Q6602 2SC3326B
Q654 2SC3326B
Q655 2SC3326B
Q6603 RN2404
Q6604 RN1404
Q6605 RN1404
D6601 MA111
D6602 MA111
D6603 MA111
PV17 LPR8029-0510F JACK2P
PV22 LPR8029-0410F
PV23 JACK
1-B2
1-D5
3-E1
1-B4
1-B7
1-B6
1-B5
1-B3
1-B8
1-B1
2-D8
5-A2
2-D3
1-B2
3-C8
3-B3
GND_VOUT
VOUT
V_TV
SW_DVI1
SW_DVI2
HDMI
A_MUTE
TV_MUTE
27WL56P
AV_JACK

(2/48)

ENG37A06GF
H001

tuner_GND

tuner_GND

TUNER-GND

TIF Type : Horizontal

1.5V (RECEIVING THE SIGNAL)
2.9V (NO SIGTNAL)

TP-12

SIF OUT

SW2

SCL1

SDA1

32V

AFT1

1-B5 SCL1
1-B5 SDA1
GND
GND
1-B2,1-D5,2-A7 5V2
1-D6,5-A1 32V

1-B5 TXT SW

V_TV 1-B3,2-C7

GND

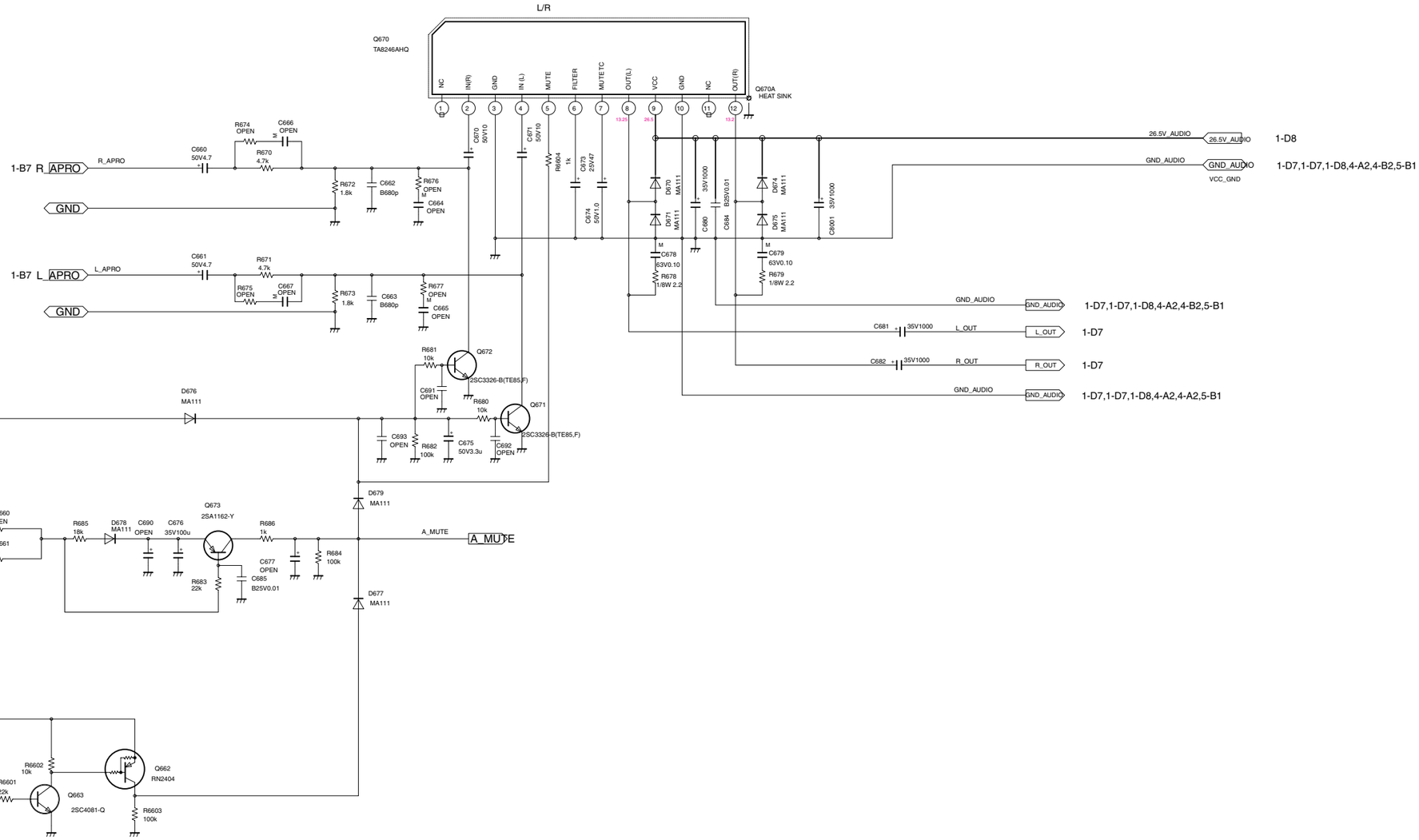
SYS SW2 1-B8

SYS SW1 1-B8

GND
SIF 1-B8
GND

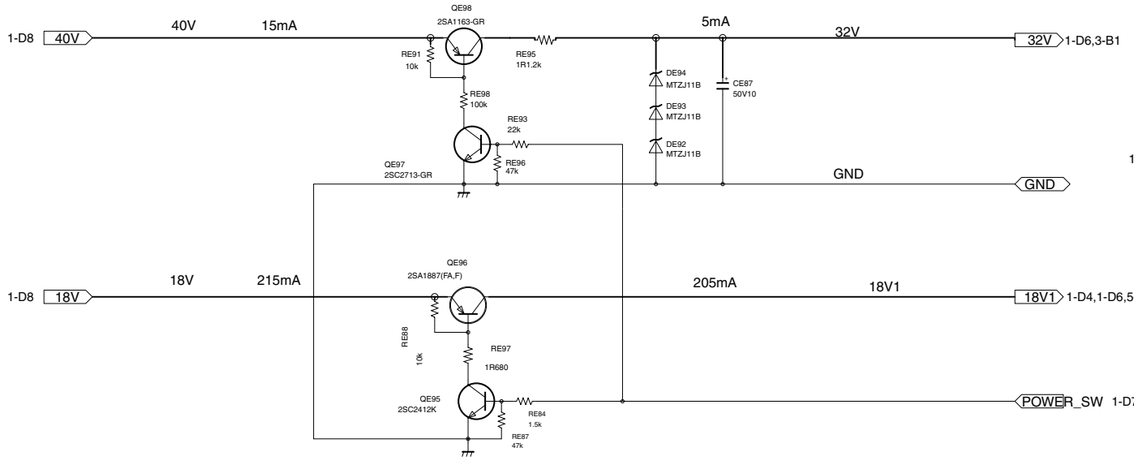
AFT1 1-B5

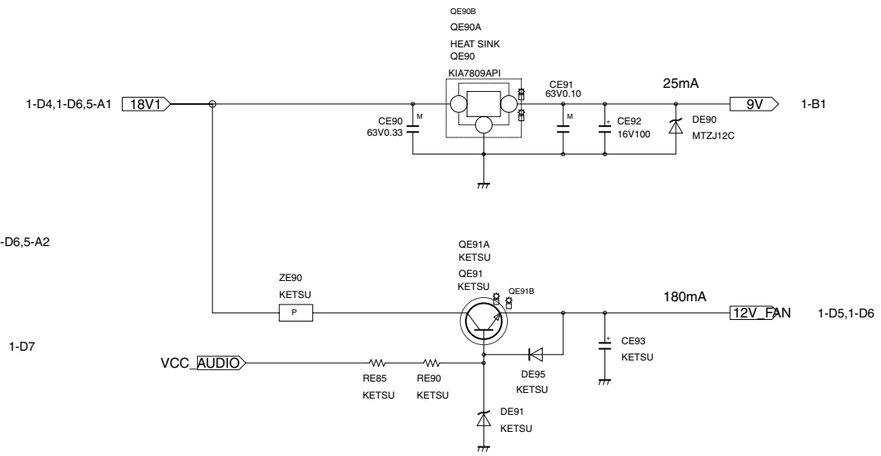
27WL56P
TIF

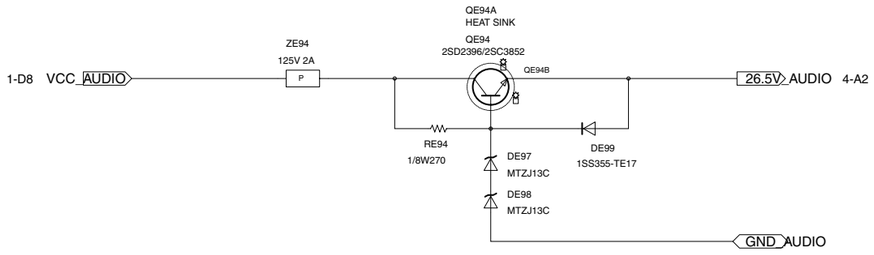


27WL56P
AUDIO_OUT

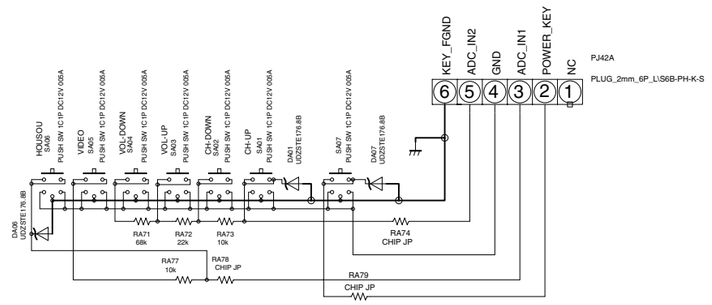
From SUB_POWER



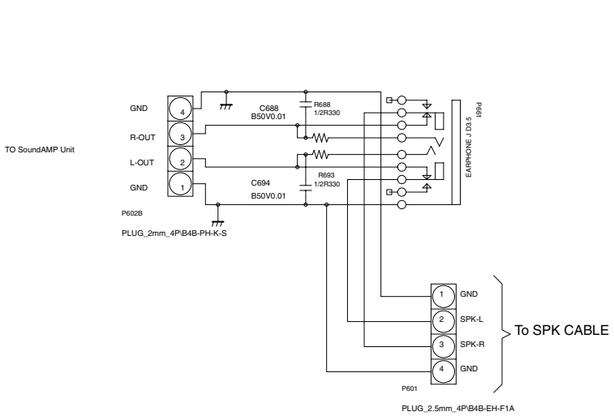


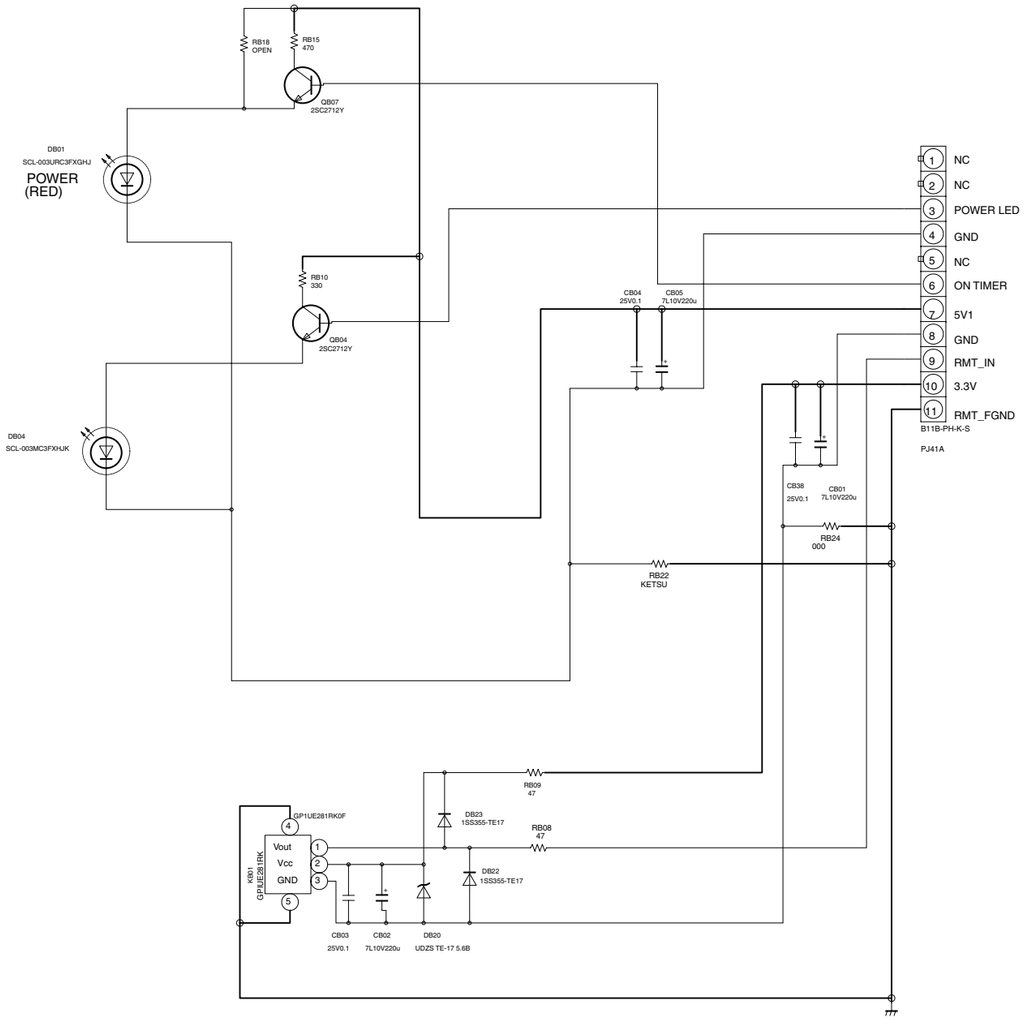


27WL56P
REGULATOR


KEY_FGND
ADC_IN2
GND
ADC_IN1
POWER_KEY
NC
6
5
4
3
2
1
PJ42A
PLUG_2mm_6P_L\S6B-PH-K-S
HOUSOU
SA06
VIDEO
SA05
VOL-DOWN
SA04
VOL-UP
SA03
CH-DOWN
SA02
CH-UP
SA01
SA07
PUSH SW 1C1P DC12V 05A
DA01
UDZSTE176.8B
DA07
UDZSTE176.8B
DA06
UDZSTE176.8B
RA71
68k
RA72
22k
RA73
10k
RA74
CHIP JP
RA77
10k
RA78
CHIP JP
RA79
CHIP JP

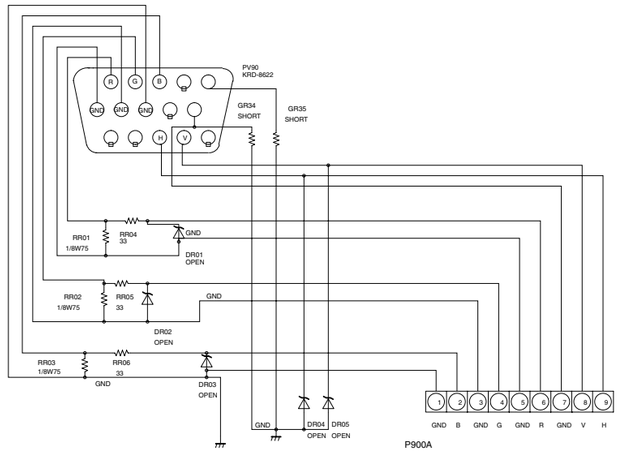
27WL56P
KEY

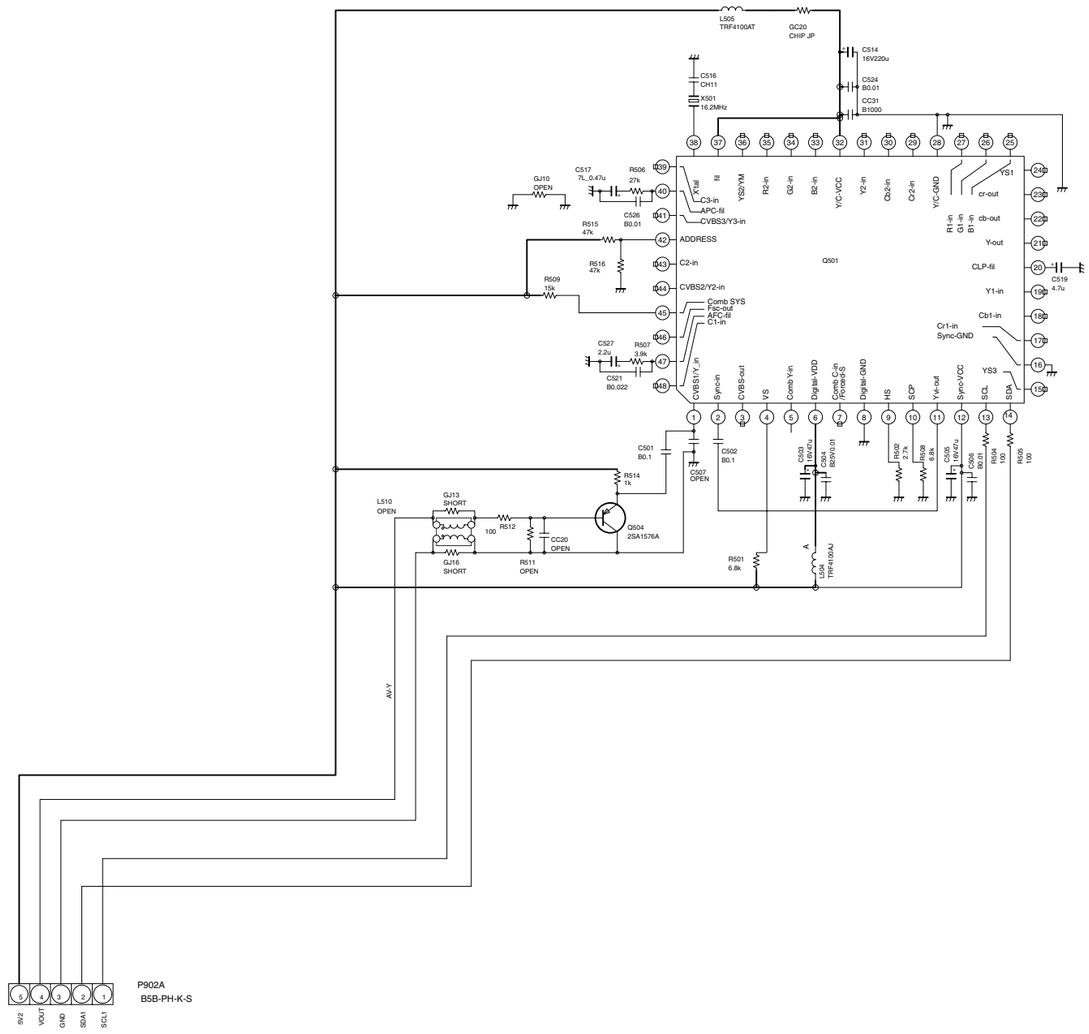




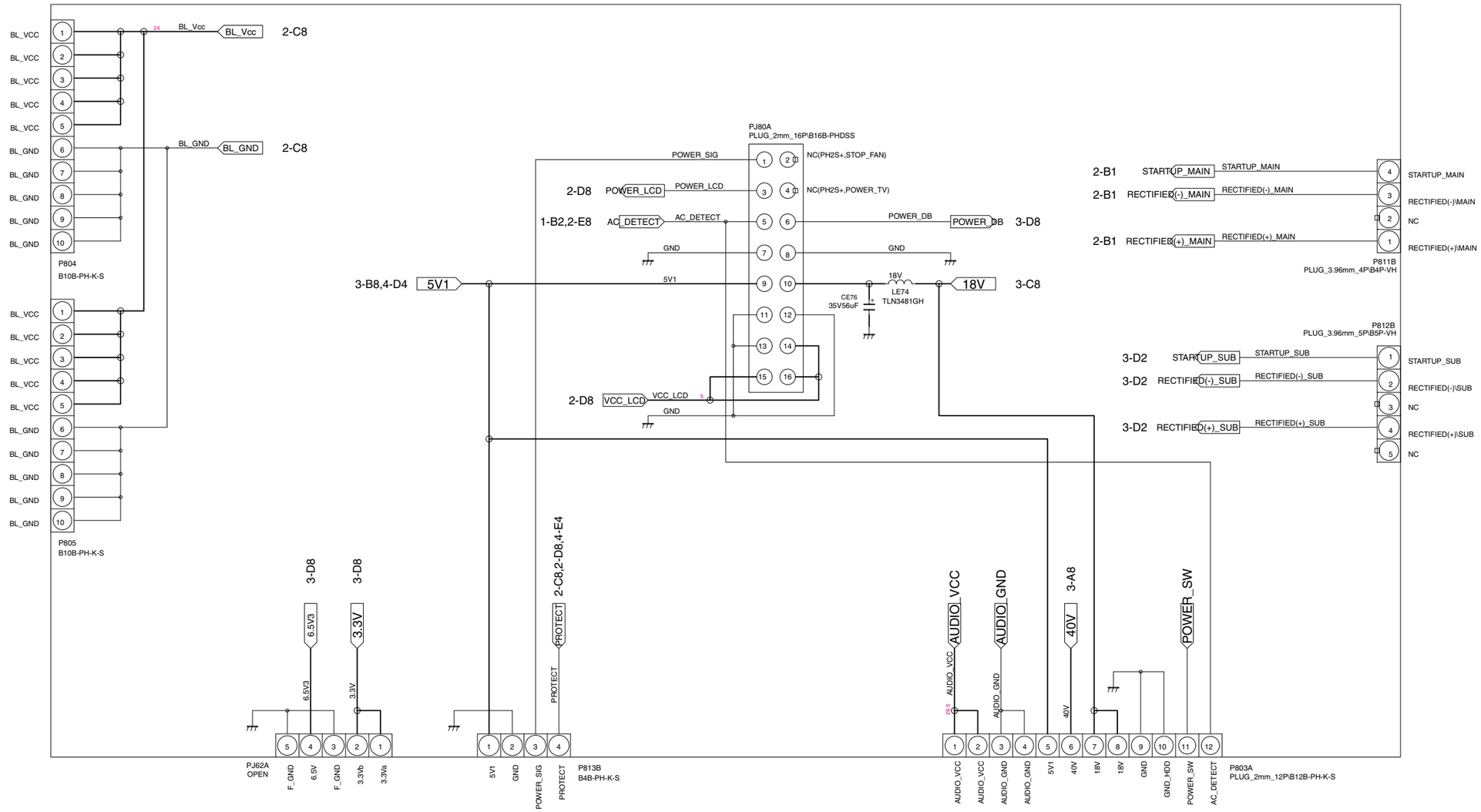
27WL56P
LED_RMT_HP

PC-DSUB

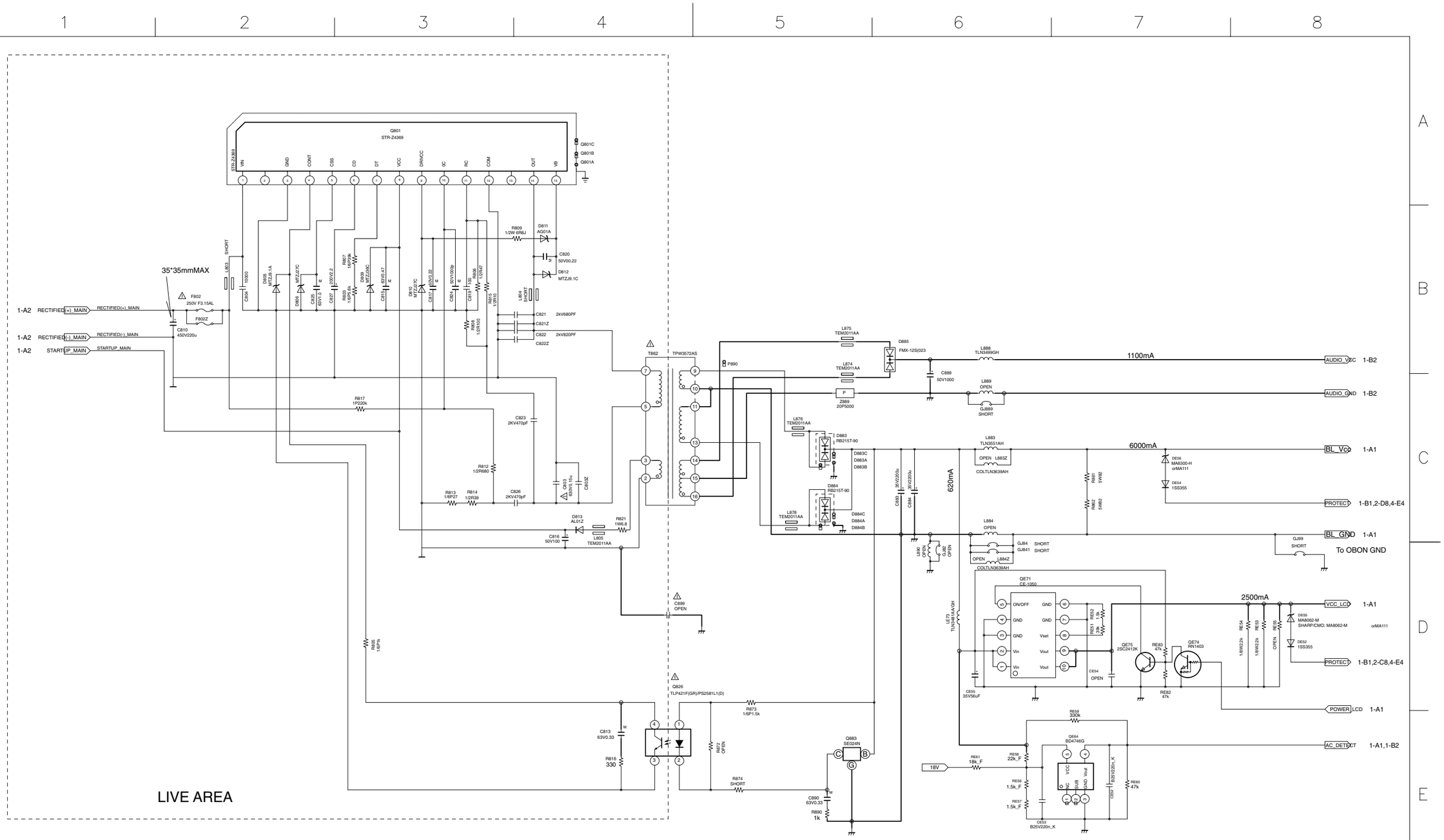


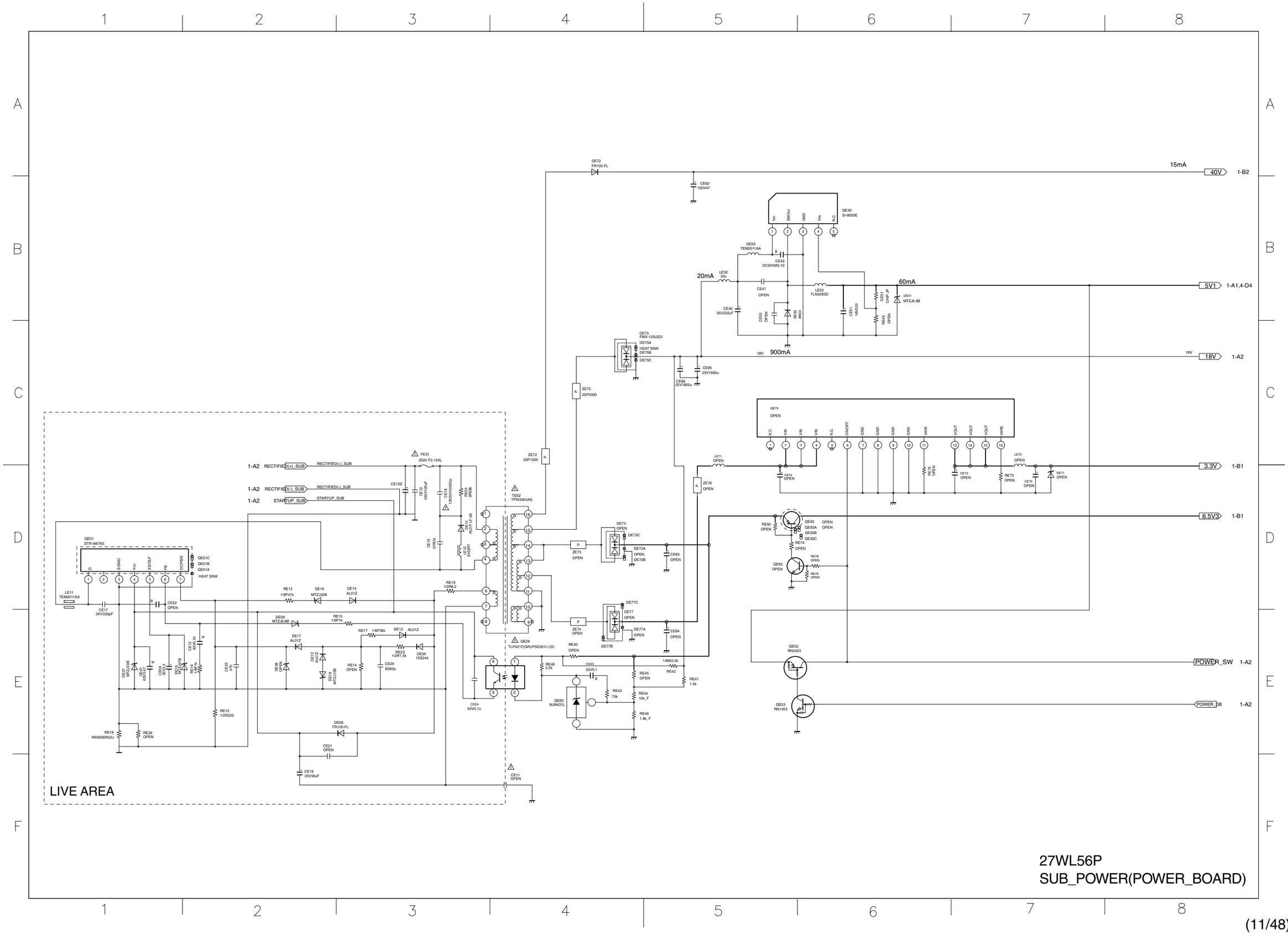


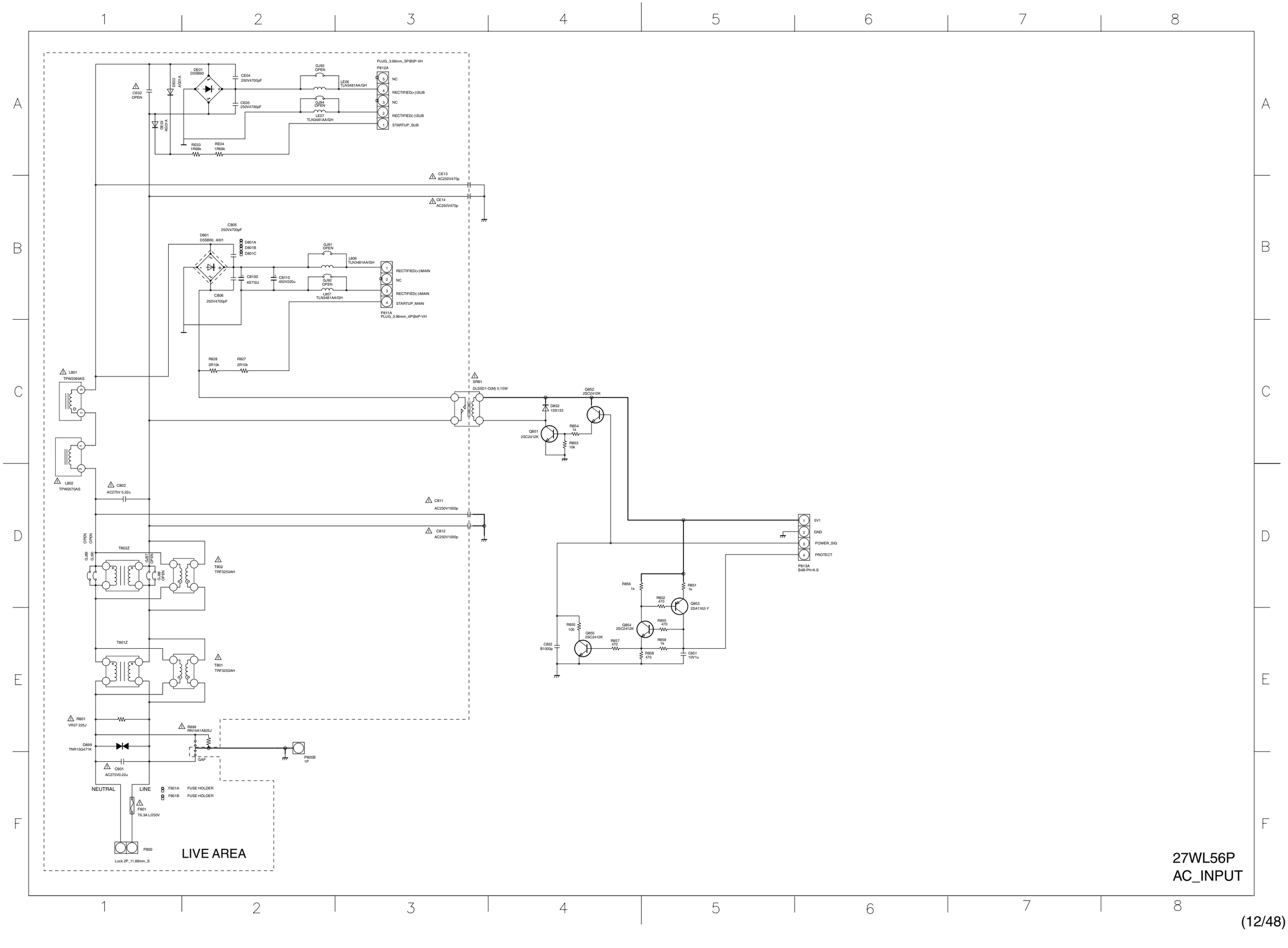
27WL56P
PC-IN_BOARD



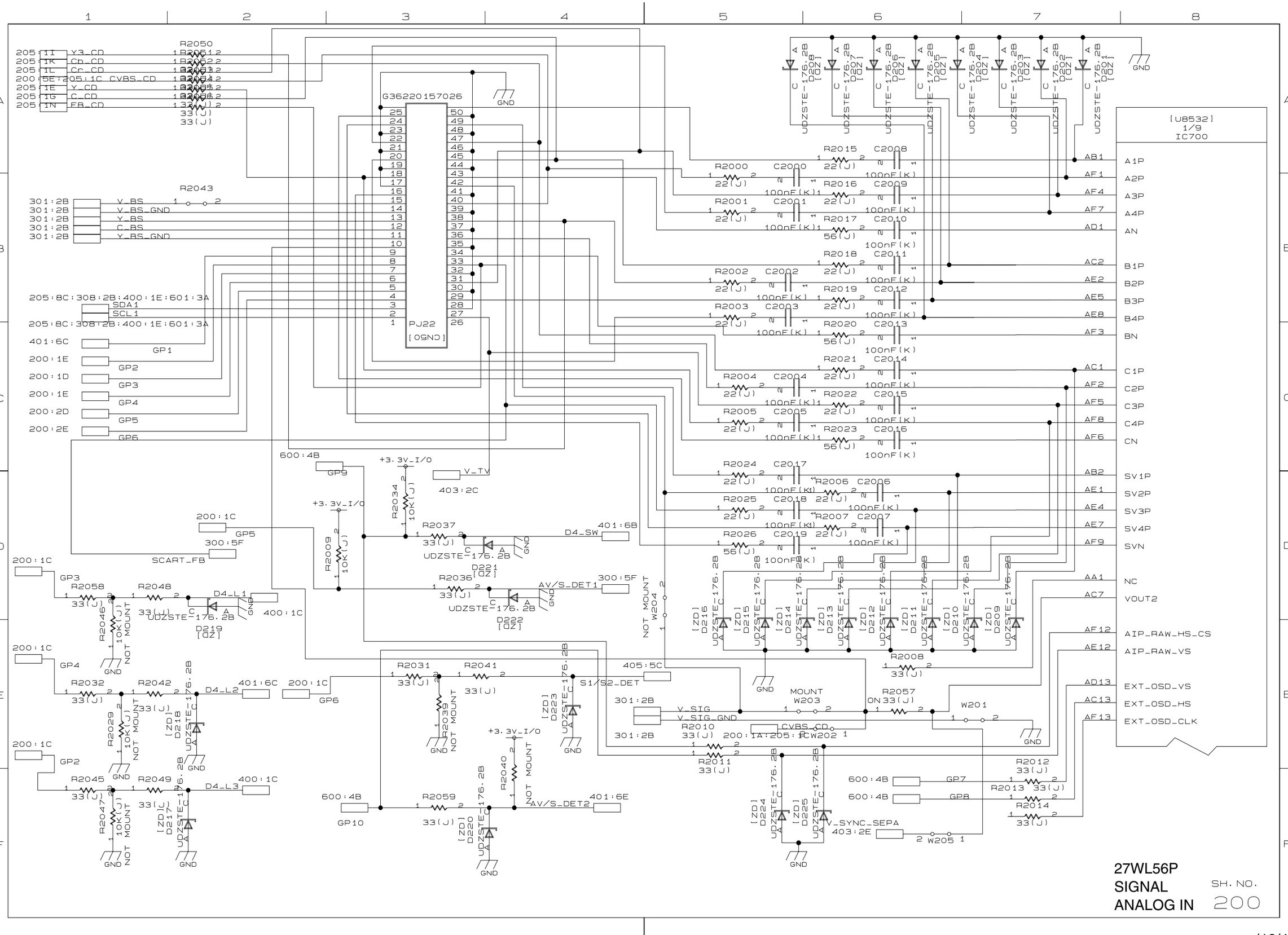
27WL56P
CONNECTOR(POWER_BOARD)

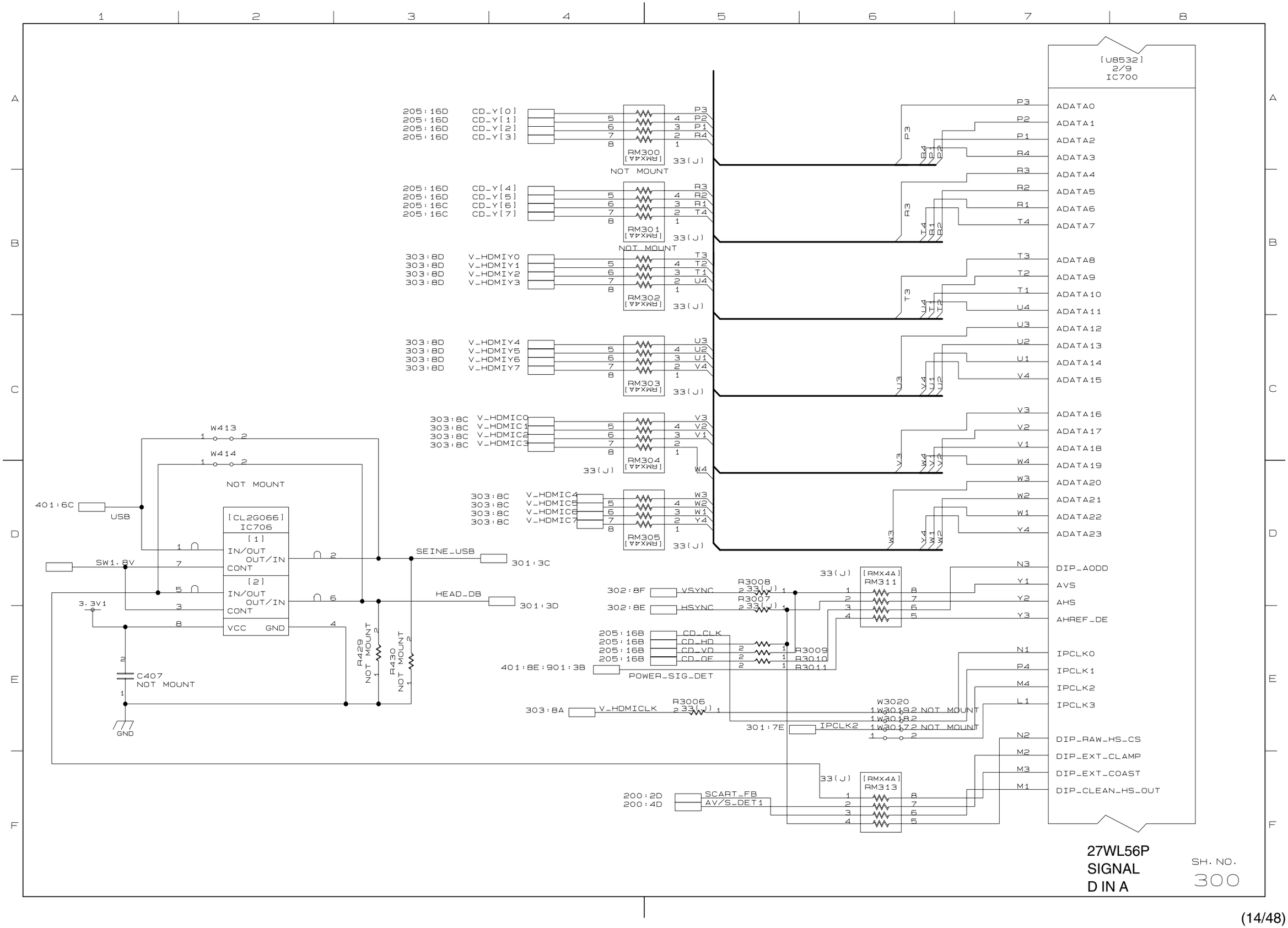

Q801
STR-Z4369
STR-Z4369
VIN
GND
CONT
CSS
CD
DT
VCC
DRAIN
OC
RC
COM
OUT
VB
Q801C
Q801B
Q801A
35*35mmMAX
F802
250V F3.15AL
F802Z
C810
450V220u
SHORT
L803
10000
D835
MA8091-A
MTZJ27C
D806
C825
63V1.0
C827
630V2.2
R830
1.6/P5.6k
D839
MTZJ39C
C815
630V.47
D812
MTZJ7.5C
C811
630V.22
C824
63V1000p
R809
1/2W 6R8J
D811
AG01A
C820
50V00.22
D812
MTZJ9.1C
R806
1/2R47
R815
1/2R15
L804
22uH/1T
C821
2kV680PF
C821Z
C822
2kV820PF
C822Z
R803
1/2R330
1-A2 RECTIFIED(+)_MAIN
RECTIFIED(+)_MAIN
1-A2 RECTIFIED(-)_MAIN
RECTIFIED(-)_MAIN
1-A2 START UP_MAIN
STARTUP_MAIN
R817
1P220k
C823
2KV470pF
R812
1/2R680
R813
1/6P27
R814
1/2R39
C826
2KV470pF
C903
630V0.1Mu
C903Z
D813
AL01Z
R821
1W6.8
C816
50V100
L805
TEM2011AA
C899
OPEN
R805
1/6P1k
T862
TPW3572AS
P890
L875
TEM2011AA
L874
TEM2011AA
Z889
20P5000
D885
FMX-12S(023
C889
50V1000
L888
TLN3499GH
L889
OPEN
GJ889
SHORT
1100mA
AUDIO_VCC
1-B2
AUDIO_GND
1-B2
L876
TEM2011AA
D883
RB215T-90
D883C
D883A
D883B
L878
TEM2011AA
D884
RB215T-90
D884C
D884A
D884B
C883
63V220u
C884
63V220u
620mA
L883
TLN3551AH
OPEN
L883Z
COLTLN3639AH
6000mA
BL_VCC
1-A1
DE56
MA8300-H
or/MA111
DE54
1SS355
PROTECT
1-B1,2-D8,4-E4
R885
2W4.7
R886
2W4.7
L884
OPEN
GJ84
SHORT
GJ841
SHORT
OPEN
L884Z
COLTLN3639AH
L890
OPEN
GJ82
OPEN
GJ99
SHORT
BL_GND
1-A1
To OBON GND
Q826
TLP421F(GR)/PS2581L1(D)
R873
1/6F1.5k
C813
63V0.33
R816
330
R872
OPEN
R874
SHORT
Q883
SE024N
C890
63V0.33
R890
1k
QE71
CE-1050
LE72
TLN3481AA/GH
ON/OFF
GND
GND
GND
Vset
Vin
Vout
Vin
Vout
RE61
2.5k
RE62
1.5k
39k
2500mA
QE75
2SC2412K
RE83
47k
QE74
RN1403
RE82
47k
CE55
35V56uF
CE54
OPEN
RE54
1/6W22k
RE53
1/6W22k
RE55
OPEN
DE55
MA8062-M
SHARP/CMO; MA8062-M
or/MA111
DE52
1SS355
VCC_LCD
1-A1
PROTECT
1-B1,2-C8,4-E4
POWER_LCD
1-A1
AC_DETECT
1-A1,1-B2
RE59
330k
QE64
BD4746G
18V
RE61
18k_F
RE58
22k_F
RE56
1.5k_F
RE57
1.5k_F
CE53
B25V220n_K
CE52
B25V220n_K
RE60
47k
LIVE AREA
27WL56P
MAIN_POWER(POWER_BOARD)

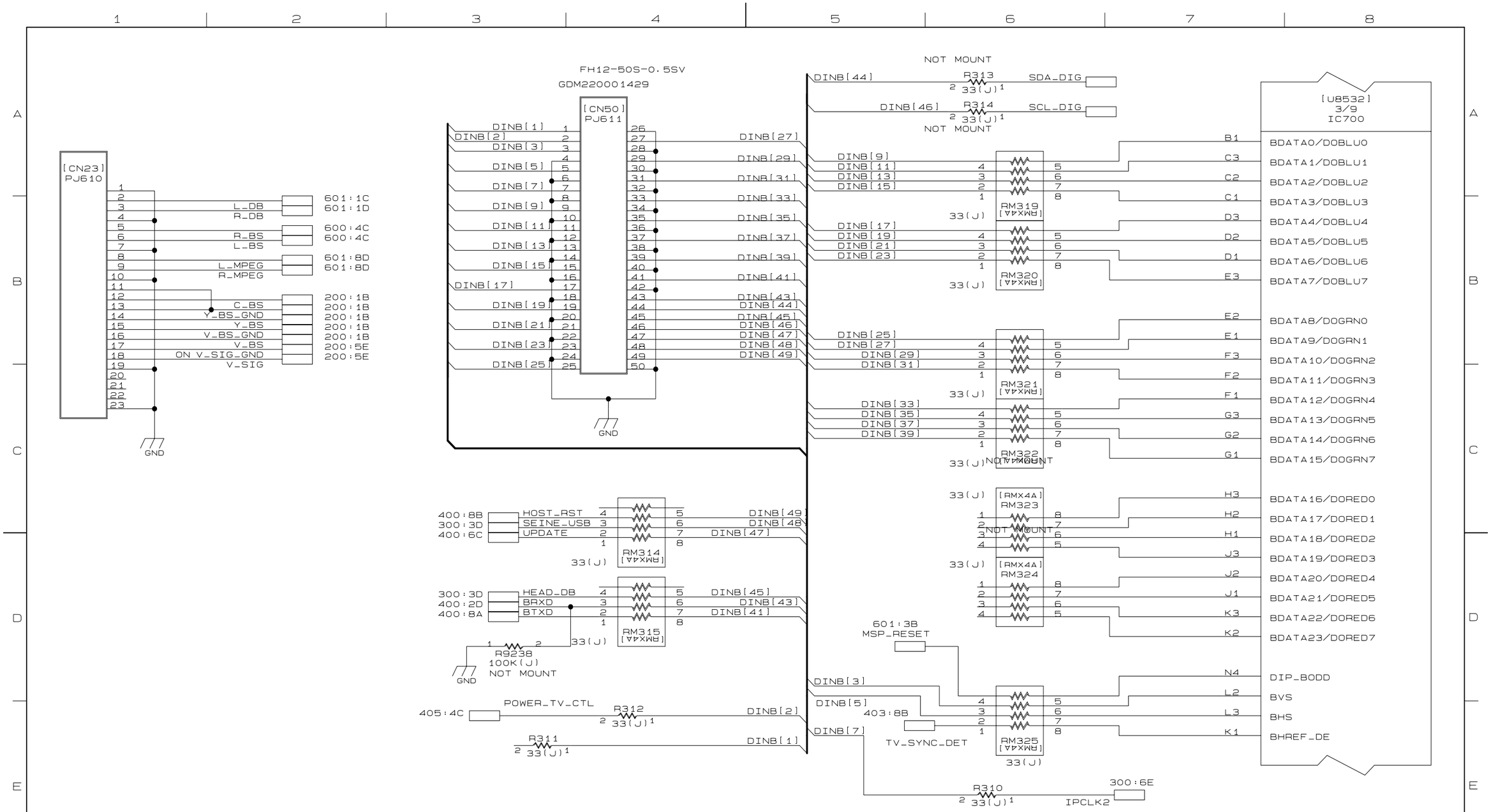

DE72
FR105-FL
CE82
100V47
15mA
40V
1-B2
QE40
SI-8050E
Vin
SWOut
GND
Vss
N.C.
QE50
TEM2011AA
CE42
DC50VM0.10
20mA
LE52
33u
CE41
OPEN
LE53
TLN3283D
60mA
5V1
1-A1,4-D4
DE51
MTZJ6.8B
GE61
CHIP JP
CE51
1W330
RE49
OPEN
CE40
35V220uF
CE50
OPEN
DE50
AK40
DE75
PMX-12S(023
DE75A
HEAT SINK
DE75B
DE75C
18V
900mA
18V
1-A2
CE85
25V1800u
CE86
25V1800u
ZE75
20P5000
QE73
OPEN
N.C.
VIN
VIN
VIN
N.C.
ON/OFF
GND
GND
GND
GND
VARI
VOUT
VOUT
VOUT
VARI
LE71
OPEN
CE74
OPEN
ZE76
OPEN
LE72
OPEN
CE73
OPEN
RE73
OPEN
CE75
OPEN
DE71
OPEN
RE75
OPEN
3.3V
1-B1
6.5V3
1-B1
RE92
OPEN
QE93
OPEN
QE93A
OPEN
QE93B
QE93C
RE74
OPEN
QE92
OPEN
RE78
OPEN
RE79
OPEN
QE52
RN2403
QE53
RN1403
POWER_SW
1-A2
POWER_DB
1-A2
FE01
250V F3.15AL
1-A2
RECTIFIED(+)_SUB
RECTIFIED(+)_SUB
1-A2
RECTIFIED(-)_SUB
RECTIFIED(-)_SUB
1-A2
STARTUP_SUB
STARTUP_SUB
CE10Z
CE10
450V120uF
CE00
250VH15000p
RE03
2R68k
DE11
RU1F-LF-A5
LE12
SHORT
CE15
OPEN
ZE72
20P1000
TE62
TPW3563AS
ZE73
OPEN
ZE74
OPEN
DE73
OPEN
DE73C
DE73A
OPEN
DE73B
DE77C
DE77
OPEN
DE77A
OPEN
DE77B
CE83
OPEN
CE84
OPEN
QE26
TLP421F(GR)/PS2581L1(D)
RE40
OPEN
RE48
2.2k
CE43
50V0.1
QE83
NJM431L
RE43
75k
RE45
OPEN
RE44
10k_F
RE46
1.8k_F
1/8W2.2k
RE42
RE41
1.5k
CE24
50V0.1u
QE01
STR-W6765
D
SGND
Vcc
SSOUP
FB
OUTBD
QE01C
QE01B
QE01A
HEAT SINK
LE11
TEM2011AA
CE17
2KV330pF
CE22
OPEN
RE13
1/6P47k
DE16
MTZJ20B
DE14
AL01Z
RE19
1/2R8.2
DE09
MTZJ6.8B
RE15
1/6P1k
RE17
1/6P36k
DE13
AL01Z
DE17
AL01Z
RE23
1/2R1.5k
DE05
1SS244
DE12
AL01Z
DE10
MTZJ10B
RE14
OPEN
CE29
B560p
DE08
OPEN
CE10
63V0.10
CE07
MTZJ33B
CE07
6.8V121
CE09
3KV473
DE15
MTZJ27B
RE16
1.5k
CE23
470
RE12
1/2R220
RE18
RK993DR22J
RE29
OPEN
DE06
FR105-FL
CE21
OPEN
CE16
35V56uF
CE11
OPEN
LIVE AREA
27WL56P
SUB_POWER(POWER_BOARD)
(11/48)



27WL56P
AC_INPUT

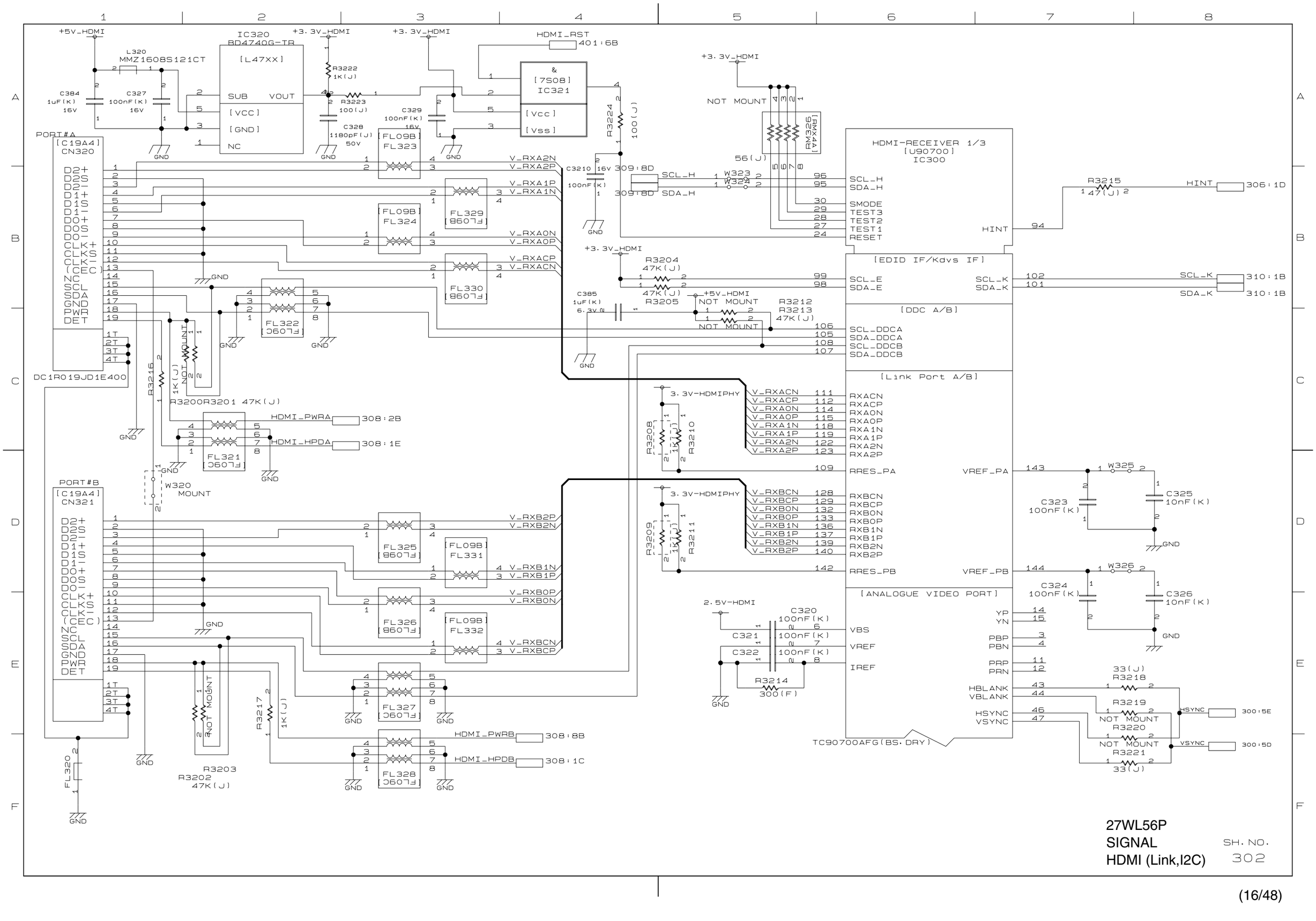

[U8532]
1/9
IC700
G36220157026
PJ22
[CN50]
GND
R2050
R2043
R2000 C2000
R2001 C2001
R2002 C2002
R2003 C2003
R2004 C2004
R2005 C2005
R2006 C2006
R2007 C2007
R2008
R2009
R2010
R2011
R2012
R2013
R2014
R2015 C2008
R2016 C2009
R2017 C2010
R2018 C2011
R2019 C2012
R2020 C2013
R2021 C2014
R2022 C2015
R2023 C2016
R2024 C2017
R2025 C2018
R2026 C2019
R2031
R2032
R2034
R2036
R2037
R2039
R2040
R2041
R2042
R2045
R2046
R2047
R2048
R2049
R2057
R2058
R2059
D4_L1
D4_L2
D4_L3
D4_SW
D219 [0Z]
D221 [0Z]
D222 [0Z]
AV/S_DET1
AV/S_DET2
S1/S2_DET
V_TV
V_SIG
V_SIG_GND
CVBS_CD
V_SYNC_SEPA
SCART_FB
SDA1
SCL1
GP1
GP2
GP3
GP4
GP5
GP6
GP7
GP8
GP9
GP10
MOUNT W203
NOT MOUNT W204
W201
W202
W205
ON
+3.3V_I/O
AB1
AF1
AF4
AF7
AD1
AC2
AE2
AE5
AE8
AF3
AC1
AF2
AF5
AF8
AF6
AB2
AE1
AE4
AE7
AF9
AA1
AC7
AF12
AE12
AD13
AC13
AF13
A1P
A2P
A3P
A4P
AN
B1P
B2P
B3P
B4P
BN
C1P
C2P
C3P
C4P
CN
SV1P
SV2P
SV3P
SV4P
SVN
NC
VOUT2
AIP_RAW_HS_CS
AIP_RAW_VS
EXT_OSD_VS
EXT_OSD_HS
EXT_OSD_CLK
27WL56P
SIGNAL
ANALOG IN
SH. NO.
200


[U8532]
2/9
IC700
205:16D CD_Y[0]
205:16D CD_Y[1]
205:16D CD_Y[2]
205:16D CD_Y[3]
RM300 [RMX4A] 33(J)
NOT MOUNT
205:16D CD_Y[4]
205:16D CD_Y[5]
205:16C CD_Y[6]
205:16C CD_Y[7]
RM301 [RMX4A] 33(J)
NOT MOUNT
303:8D V_HDMIY0
303:8D V_HDMIY1
303:8D V_HDMIY2
303:8D V_HDMIY3
RM302 [RMX4A] 33(J)
303:8D V_HDMIY4
303:8D V_HDMIY5
303:8D V_HDMIY6
303:8D V_HDMIY7
RM303 [RMX4A] 33(J)
303:8C V_HDMIC0
303:8C V_HDMIC1
303:8C V_HDMIC2
303:8C V_HDMIC3
RM304 [RMX4A] 33(J)
303:8C V_HDMIC4
303:8C V_HDMIC5
303:8C V_HDMIC6
303:8C V_HDMIC7
RM305 [RMX4A] 33(J)
ADATA0
ADATA1
ADATA2
ADATA3
ADATA4
ADATA5
ADATA6
ADATA7
ADATA8
ADATA9
ADATA10
ADATA11
ADATA12
ADATA13
ADATA14
ADATA15
ADATA16
ADATA17
ADATA18
ADATA19
ADATA20
ADATA21
ADATA22
ADATA23
W413
W414
NOT MOUNT
401:6C
USB
[CL2G066]
IC706
IN/OUT
OUT/IN
CONT
SW1.8V
3.3V1
VCC
GND
SEINE_USB
301:3C
HEAD_DB
301:3D
C407
NOT MOUNT
R429
NOT MOUNT
R430
NOT MOUNT
GND
302:8F VSYNC
302:8E HSYNC
R3008 33(J)
R3007 33(J)
RM311 [RMX4A] 33(J)
205:16B CD_CLK
205:16B CD_HD
205:16B CD_VD
205:16B CD_OE
R3009
R3010
R3011
401:8E:901:3B
POWER_SIG_DET
303:8A V_HDMICLK
R3006 33(J)
301:7E IPCLK2
W3020
W3019 NOT MOUNT
W3018
W3017 NOT MOUNT
N3 DIP_AODD
Y1 AVS
Y2 AHS
Y3 AHREF_DE
N1 IPCLK0
P4 IPCLK1
M4 IPCLK2
L1 IPCLK3
N2 DIP_RAW_HS_CS
M2 DIP_EXT_CLAMP
M3 DIP_EXT_COAST
M1 DIP_CLEAN_HS_OUT
200:2D SCART_FB
200:4D AV/S_DET1
RM313 [RMX4A] 33(J)
27WL56P
SIGNAL
D IN A
SH. NO.
300



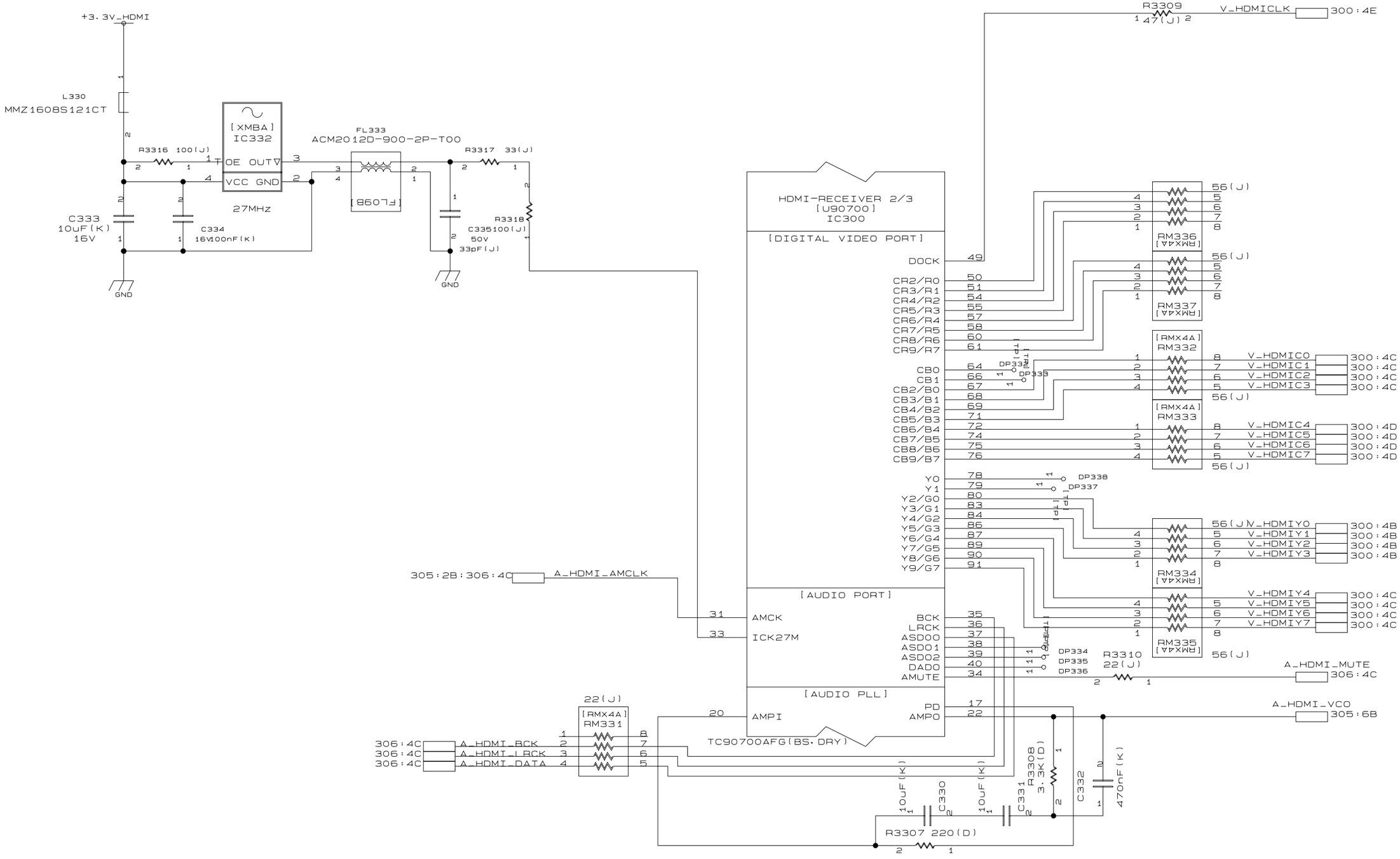
27WL56P
SIGNAL
D IN B

SH. NO.
301



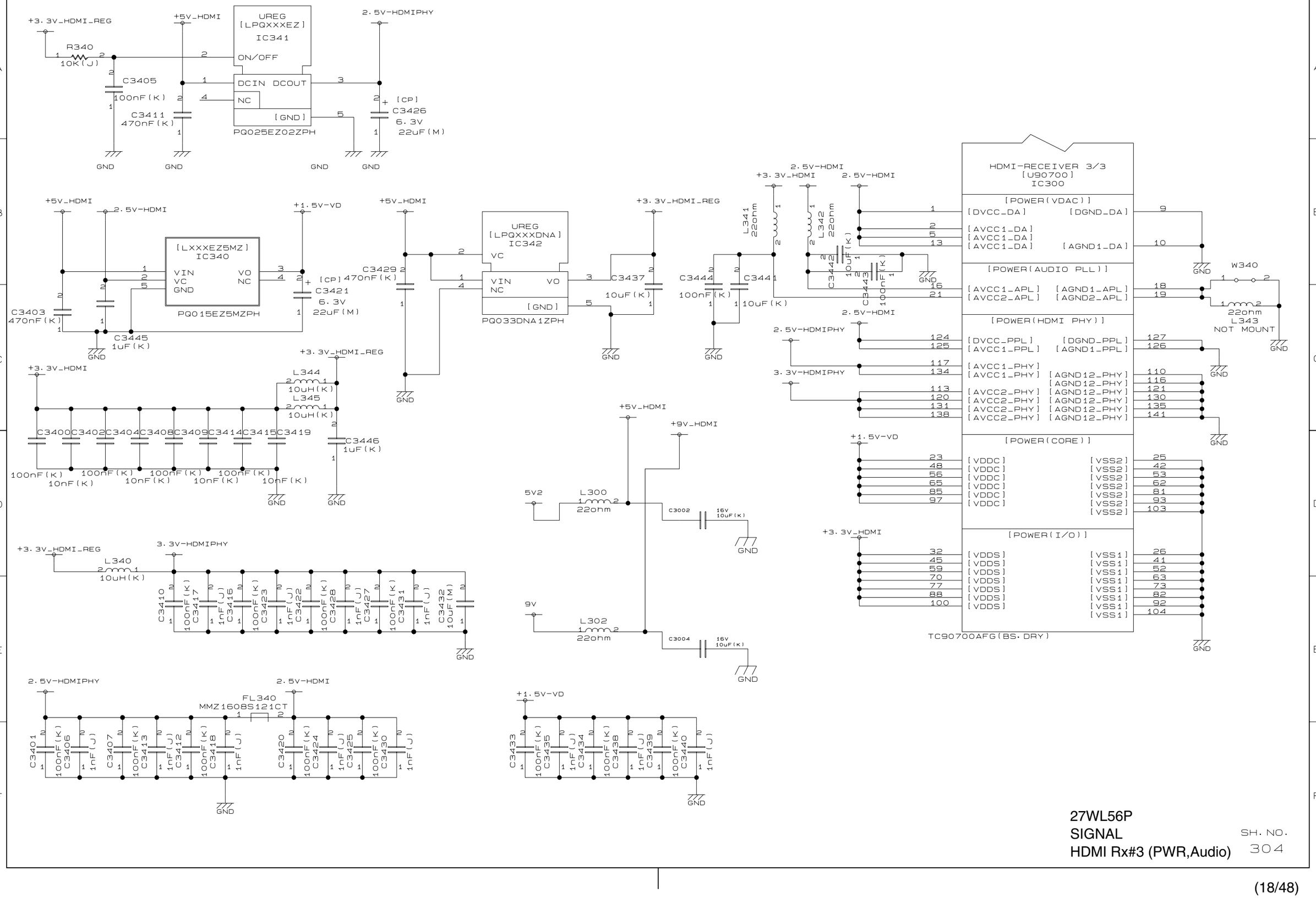
27WL56P
SIGNAL
HDMI (Link,I2C)

SH. NO.
302



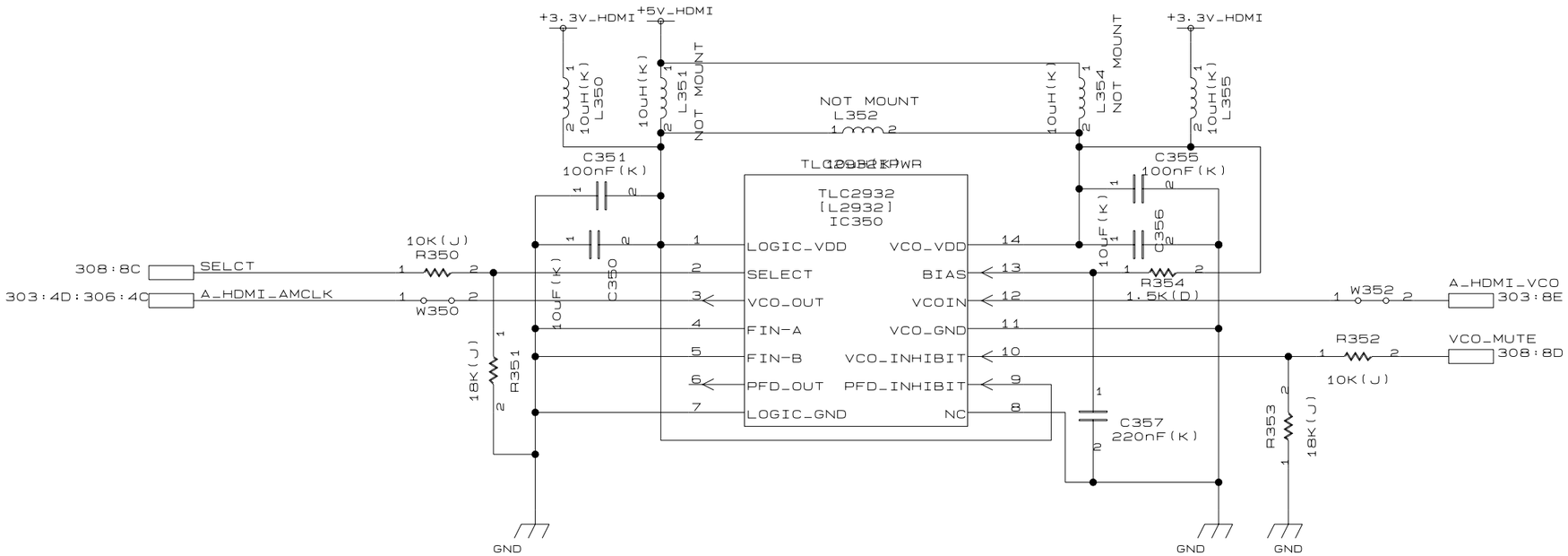
27WL56P
SIGNAL
HDMI Rx#2 (Video/Audio)

SH. NO.
303



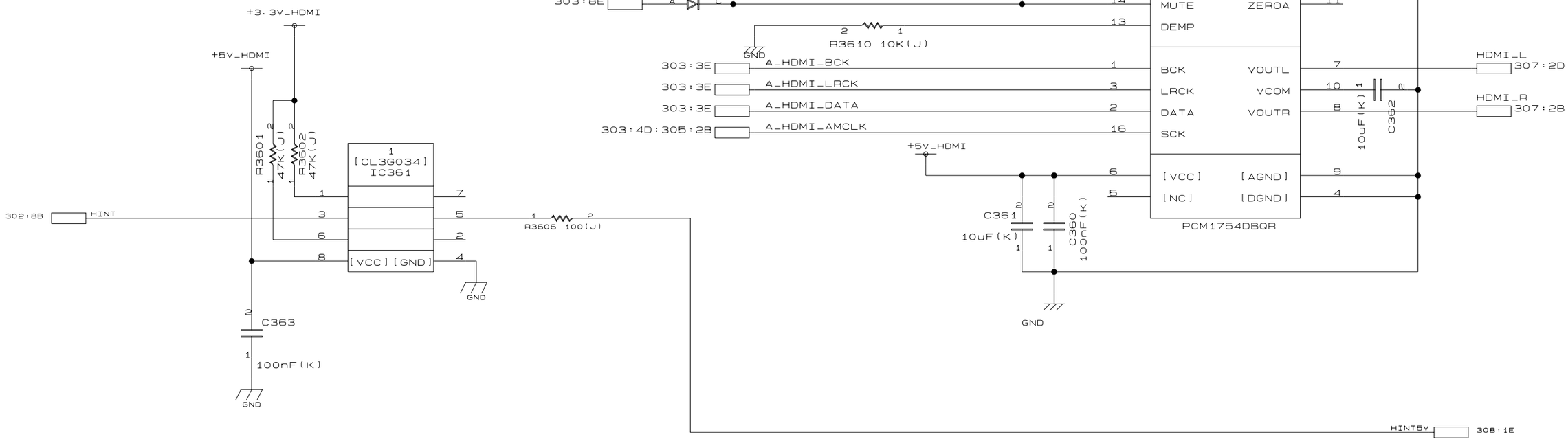
27WL56P
SIGNAL
HDMI Rx#3 (PWR,Audio)

SH. NO.
304



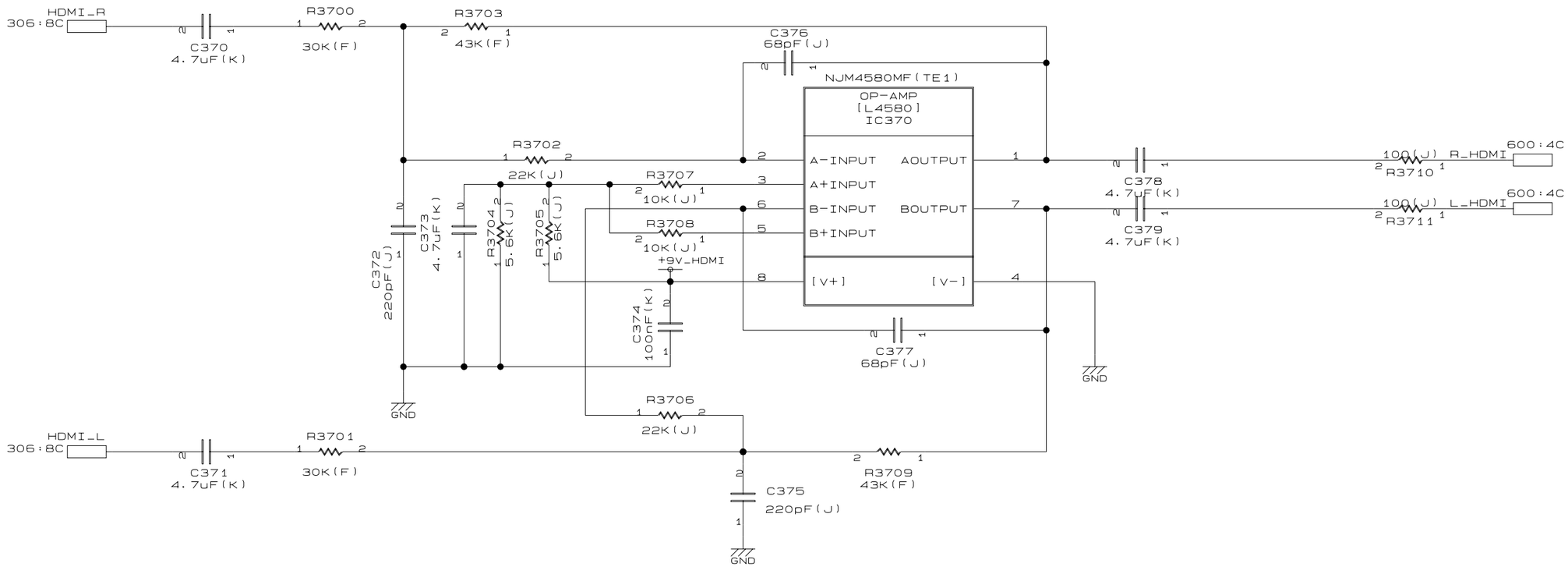
27WL56P
SIGNAL
HDMI AUDIO PLL

SH. NO.
305



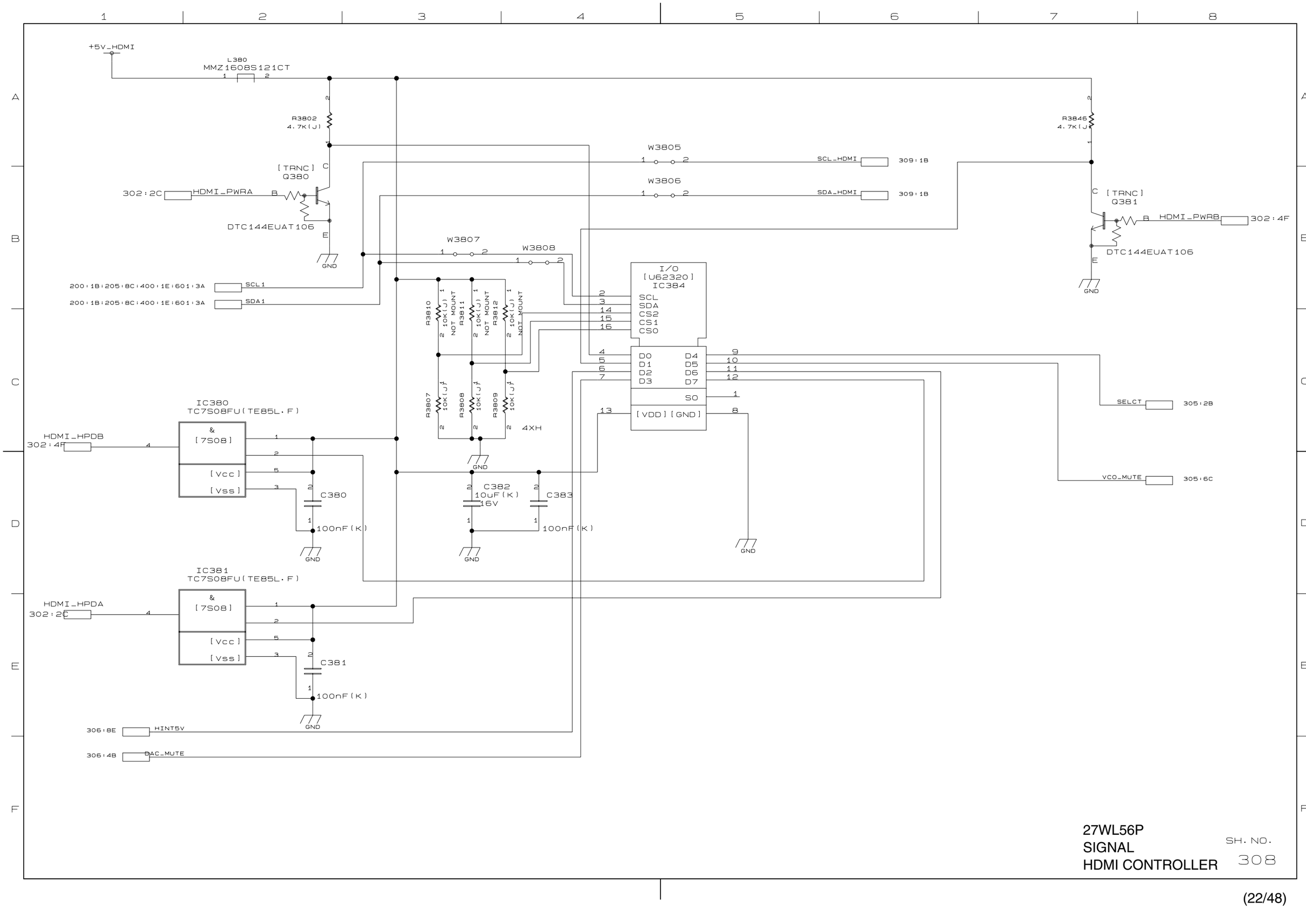
27WL56P
SIGNAL
HDMI AUDIO DAC

SH. NO.
306



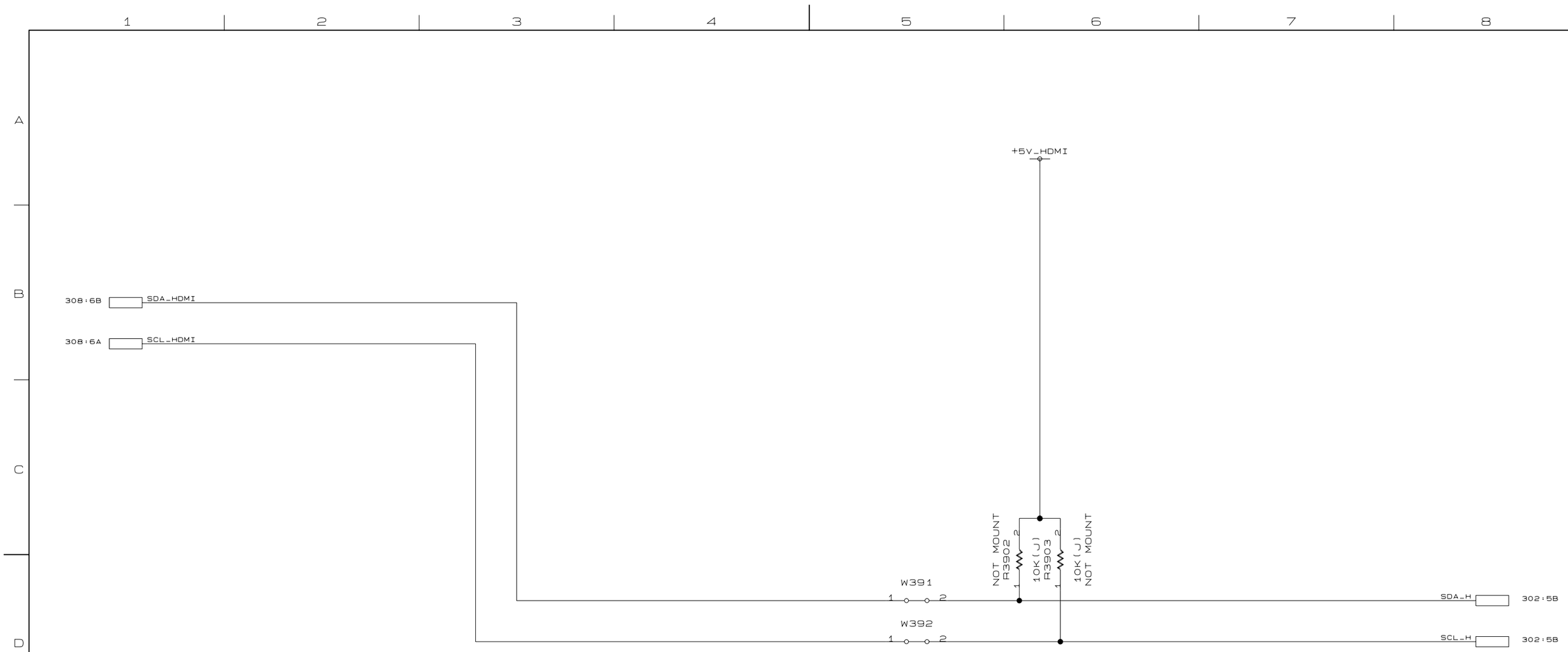
27WL56P
SIGNAL
HDMI AUDIO OUT

SH. NO.
307



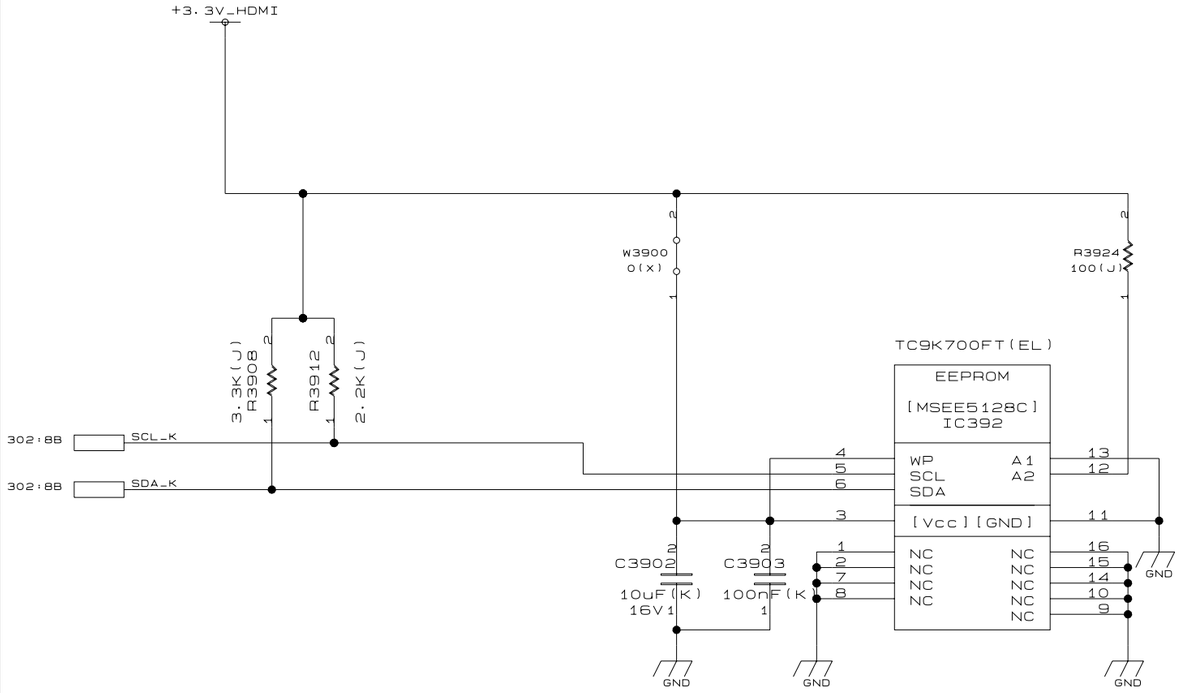
27WL56P
SIGNAL
HDMI CONTROLLER

SH. NO.
308



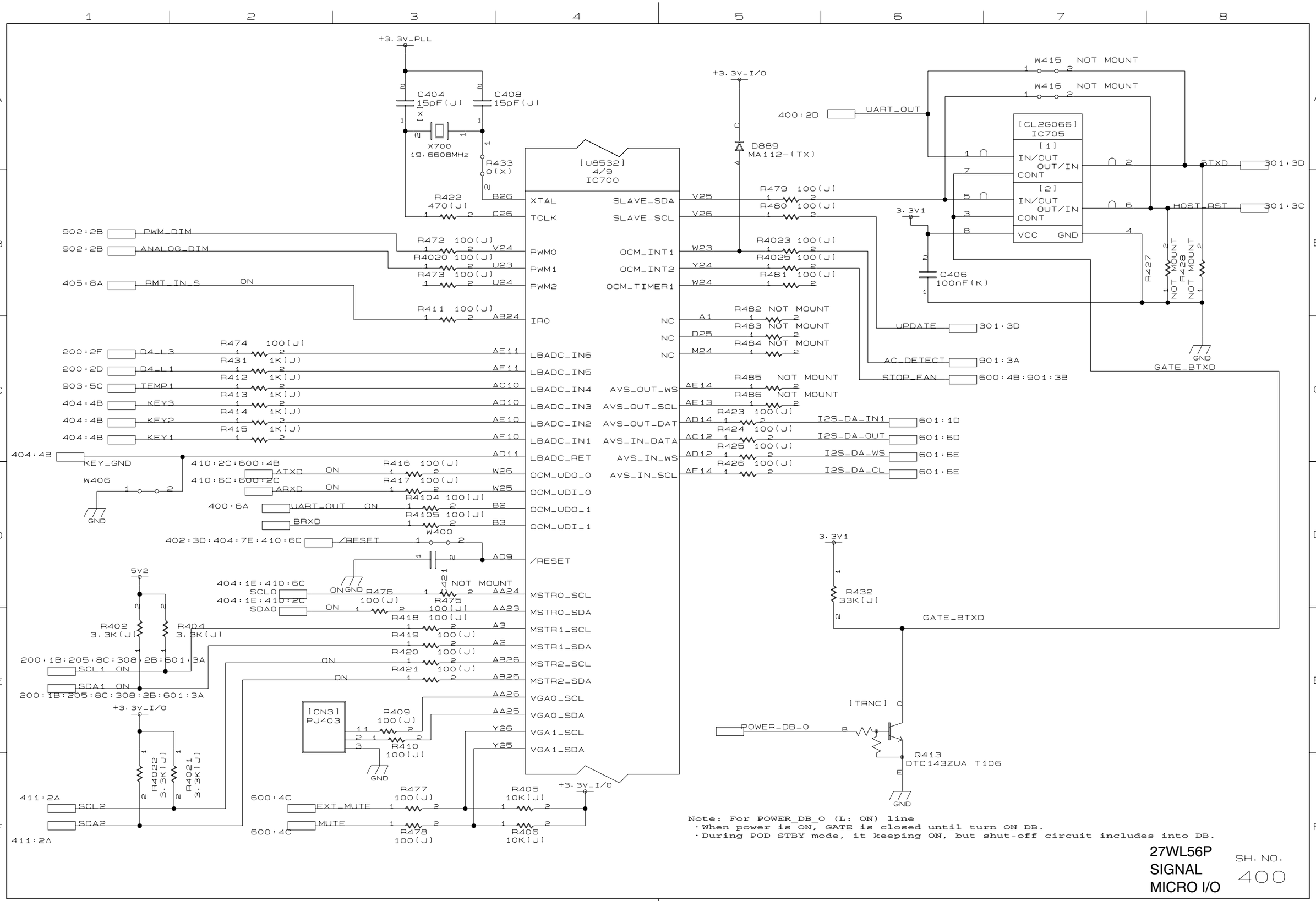
27WL56P
SIGNAL
EEPROM1

SH. NO.
309



27WL56P
SIGNAL
EEPROM2

SH. NO.
310


+3.3V_PLL
C404 15pF(J)
C408 15pF(J)
X700 19.6608MHz
R433 0(X)
R422 470(J)
[U8532] 4/9 IC700
XTAL
TCLK
PWM0
PWM1
PWM2
IRO
LBADC_IN6
LBADC_IN5
LBADC_IN4
LBADC_IN3
LBADC_IN2
LBADC_IN1
LBADC_RET
SLAVE_SDA
SLAVE_SCL
OCM_INT1
OCM_INT2
OCM_TIMER1
NC
NC
NC
AVS_OUT_WS
AVS_OUT_SCL
AVS_OUT_DAT
AVS_IN_DATA
AVS_IN_WS
AVS_IN_SCL
OCM_UDO_0
OCM_UDI_0
OCM_UDO_1
OCM_UDI_1
/RESET
MSTR0_SCL
MSTR0_SDA
MSTR1_SCL
MSTR1_SDA
MSTR2_SCL
MSTR2_SDA
VGA0_SCL
VGA0_SDA
VGA1_SCL
VGA1_SDA
902:2B PWM_DIM
902:2B ANALOG_DIM
405:8A RMT_IN_S
ON
R472 100(J)
R4020 100(J)
R473 100(J)
R411 100(J)
200:2F D4_L3
200:2D D4_L1
903:5C TEMP1
404:4B KEY3
404:4B KEY2
404:4B KEY1
404:4B KEY_GND
R474 100(J)
R431 1K(J)
R412 1K(J)
R413 1K(J)
R414 1K(J)
R415 1K(J)
W406
GND
410:2C:600:4B
410:6C:600:2C
400:6A
402:3D:404:7E:410:6C
ATXD
ARXD
UART_OUT
BRXD
/RESET
R416 100(J)
R417 100(J)
R4104 100(J)
R4105 100(J)
W400
5V2
404:1E:410:6C
404:1E:410:2C
SCL0
SDA0
R476 100(J)
R475 100(J)
R418 100(J)
R419 100(J)
R420 100(J)
R421 100(J)
NOT MOUNT
R402 3.3K(J)
R404 3.3K(J)
200:1B:205:8C:308:2B:601:3A
SCL1
SDA1
+3.3V_I/O
R4022 3.3K(J)
R4021 3.3K(J)
411:2A
SCL2
SDA2
[CN3] PJ403
R409 100(J)
R410 100(J)
600:4C
EXT_MUTE
MUTE
R477 100(J)
R478 100(J)
R405 10K(J)
R406 10K(J)
+3.3V_I/O
D889 MA112-(TX)
400:2D
UART_OUT
W415 NOT MOUNT
W416 NOT MOUNT
[CL2G066] IC705
[1] IN/OUT OUT/IN CONT
[2] IN/OUT OUT/IN CONT
VCC GND
3.3V1
C406 100nF(K)
R427
R428 NOT MOUNT
BTXD
HOST_RST
301:3D
301:3C
GND
GATE_BTXD
R479 100(J)
R480 100(J)
R4023 100(J)
R4025 100(J)
R481 100(J)
R482 NOT MOUNT
R483 NOT MOUNT
R484 NOT MOUNT
R485 NOT MOUNT
R486 NOT MOUNT
UPDATE
AC_DETECT
STOP_FAN
301:3D
901:3A
600:4B:901:3B
R423 100(J)
R424 100(J)
R425 100(J)
R426 100(J)
I2S_DA_IN1
I2S_DA_OUT
I2S_DA_WS
I2S_DA_CL
601:1D
601:6D
601:6E
601:6E
R432 33K(J)
GATE_BTXD
[TRNC] C
POWER_DB_O
B
Q413 DTC143ZUA T106
E
GND
Note: For POWER_DB_O (L: ON) line
·When power is ON, GATE is closed until turn ON DB.
·During POD STBY mode, it keeping ON, but shut-off circuit includes into DB.
27WL56P
SIGNAL
MICRO I/O
SH. NO.
400

402:3A;409:1B

/OCM_RE, /OCM_WE, /ROM_CS, OCMADDR[0-20], OCMDATA[0-7]

[U8532]
7/9
IC700

R487 33(J)
R440 33(J)

OCMADDR[20]
OCMADDR[19]
OCMADDR[18]
OCMADDR[17]
OCMADDR[16]
OCMADDR[15]
OCMADDR[14]
OCMADDR[13]
OCMADDR[12]
OCMADDR[11]
OCMADDR[10]
OCMADDR[9]
OCMADDR[8]
OCMADDR[7]
OCMADDR[6]
OCMADDR[5]
OCMADDR[4]
OCMADDR[3]
OCMADDR[2]
OCMADDR[1]
OCMADDR[0]

RM400 [RMX4A] 33(J)
RM401 [RMX4A] 33(J)
RM402 [RMX4A] 33(J)
RM403 [RMX4A] 33(J)
RM404 [RMX4A] 33(J)

AF15	OCMADDR21	PBIAS	U26
AE15	OCMADDR20	PPWR	U25
AD15	OCMADDR19		
AC15	OCMADDR18	GPIO47	R25
AF16	OCMADDR17	GPIO46	R24
AE16	OCMADDR16	GPIO45	R23
AD16	OCMADDR15	GPIO44	T26
AF17	OCMADDR14	JTAG_BS_TDO	T25
AE17	OCMADDR13	JTAG_BS_TMS	T24
AD17	OCMADDR12		
AC17	OCMADDR11	OCMCS2n	AC24
AF18	OCMADDR10	OCMCS1n	AD26
AE18	OCMADDR9	OCMCS0n	AD25
AD18	OCMADDR8	ROMCSn	AD24
AF19	OCMADDR7	OCMWEn	AC26
AE19	OCMADDR6	OCMREn	AC25
AD19	OCMADDR5		
AC19	OCMADDR4	OCMDATA0	AE26
AF20	OCMADDR3	OCMDATA1	AE25
AE20	OCMADDR2	OCMDATA2	AE24
AD20	OCMADDR1	OCMDATA3	AF26
AF21	OCMADDR0	OCMDATA4	AF25
		OCMDATA5	AF24
		OCMDATA6	AC23
		OCMDATA7	AD23
		OCMDATA8	AE23
		OCMDATA9	AF23
		OCMDATA10	AD22
		OCMDATA11	AE22
		OCMDATA12	AF22
		OCMDATA13	AC21
		OCMDATA14	AD21
		OCMDATA15	AE21

R441 100(J) — BL_ONOFF — 902:1D
R442 100(J) — POWER_LCD — 901:3B
R443 100(J) — D4_SW — 200:4D
R444 100(J) NOT MOUNT — STBY_RST — 405:3E
R445 100(J) — HDMI_RST — 302:4A
R446 100(J) — POWER_KEY_S — 405:8A
R447 100(J) — GP1 — 200:1C
R448 100(J) — BS_TV_MUTE — 600:4C
R488 100(J) — USB — 300:1D
R489 100(J) — MAIN_SYNC_DET — 403:8E
R449 1K(J) — D4_L2 — 200:2E

+3.3V_I/O
R40710K(J)

[RMX4A] RM407 33(J)
/ROM_CS
/OCM_WE
/OCM_RE

[RMX4A] RM405 33(J)
OCMDATA[0]
OCMDATA[1]
OCMDATA[2]
OCMDATA[3]

[RMX4A] RM406 33(J)
OCMDATA[4]
OCMDATA[5]
OCMDATA[6]
OCMDATA[7]

R4029 100(J) — SW_DVI1 — 600:4B
R4030 100(J) — AV/S_DET2 — 200:4F
R4031 100(J) NOT MOUNT (AV/S DET3)
R4032 33K(J)
R4033 100(J) — SW_DVI2 — 600:4B
R4034 100(J) — LED3 — 404:8B
R4035 100(J) — LED4 — 404:8B
R4036 100(J) — 50HZ_60HZ — 903:1A

+3.3V_I/O
R4026 33K(J)
[TRP] Q404 2SA1586-Y(TE85L,F)
R4027 1K(J)
300:4E:901:3B
POWER_SIG_DET
R4028 220K(J)
C478 470pF(J)
GND

27WL56P
SIGNAL
OCM MEMORY I/F

SH. NO. 401

401:1A;409:1B

/OCM_RE. /OCM_WE. /ROM_CS. OCMADDR[0-20]. OCMDATA[0-7]

MX26LV800TTC-70G

[MFLV800AT]
IC404

OCMADDR[1] 25
OCMADDR[2] 24
OCMADDR[3] 23
OCMADDR[4] 22
OCMADDR[5] 21
OCMADDR[6] 20
OCMADDR[7] 19
OCMADDR[8] 18
OCMADDR[9] 8
OCMADDR[10] 7
OCMADDR[11] 6
OCMADDR[12] 5
OCMADDR[13] 4
OCMADDR[14] 3
OCMADDR[15] 2
OCMADDR[16] 1
OCMADDR[17] 48
OCMADDR[18] 17
OCMADDR[19] 16

A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
A16
A17
A18

/ROM_CS 26 → CE
/OCM_RE 28 → OE
/OCM_WE 11 → WE
47 → BYTE
12 → RESET
RY/BY → 15

GND

400:3D:404:7E:410:6C
/RESET 1 2 W411 MOUNT

R4086 NOT MOUNT

C464 NOT MOUNT

+3.3V_I/O

OCMADDR[20] 9
13 NC NC
37 VCC
27 VSS
46 VSS

C410 10uF(K) 6.3V

C411 100nF(K) 16V

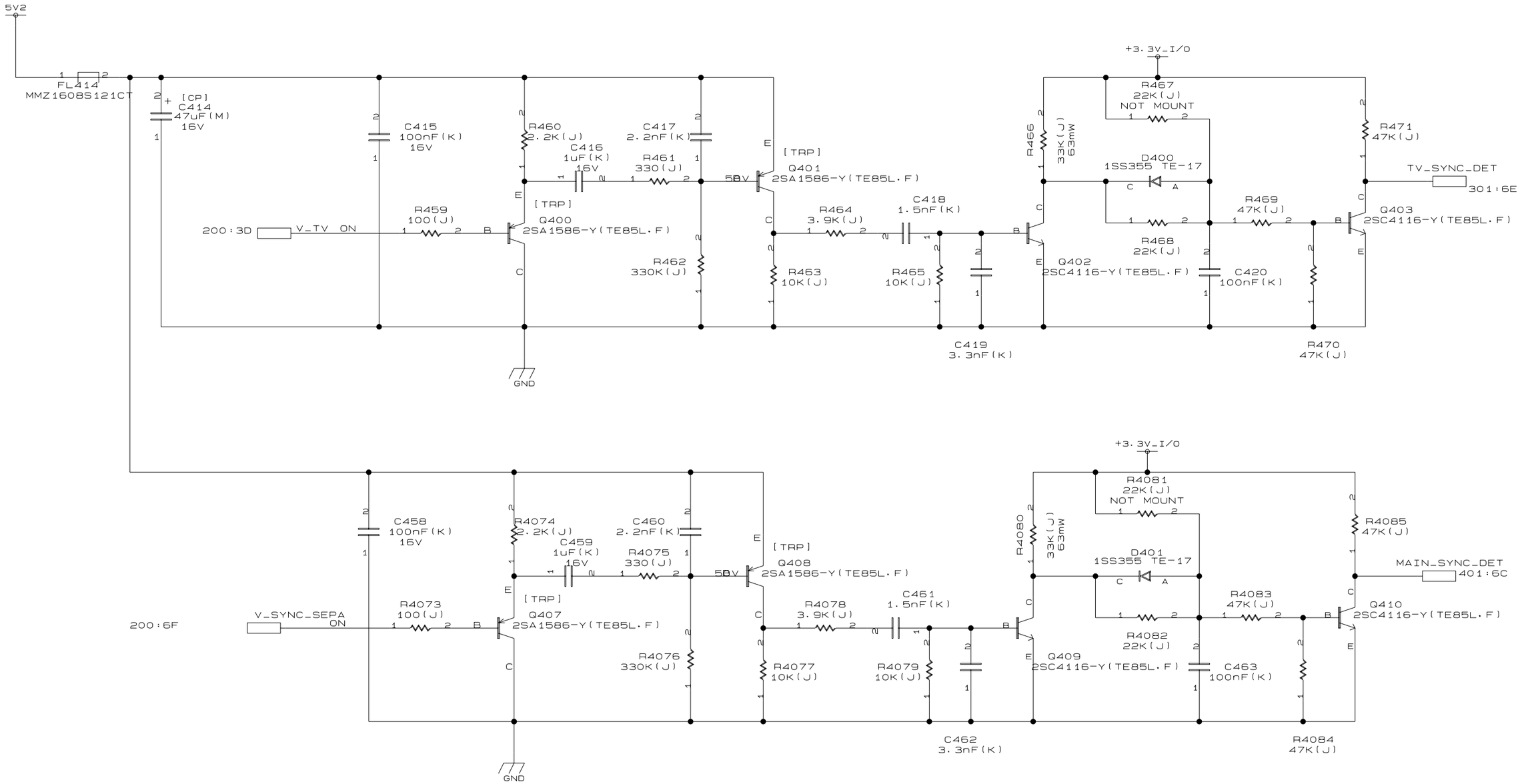
DQ0 29 OCMDATA[0]
DQ1 31 OCMDATA[1]
DQ2 33 OCMDATA[2]
DQ3 35 OCMDATA[3]
DQ4 38 OCMDATA[4]
DQ5 40 OCMDATA[5]
DQ6 42 OCMDATA[6]
DQ7 44 OCMDATA[7]
DQ8 30
DQ9 32
DQ10 34
DQ11 36
DQ12 39
DQ13 41
DQ14 43
DQ15/A-1 45 OCMADDR[0]

NC 10
NC 14

GND

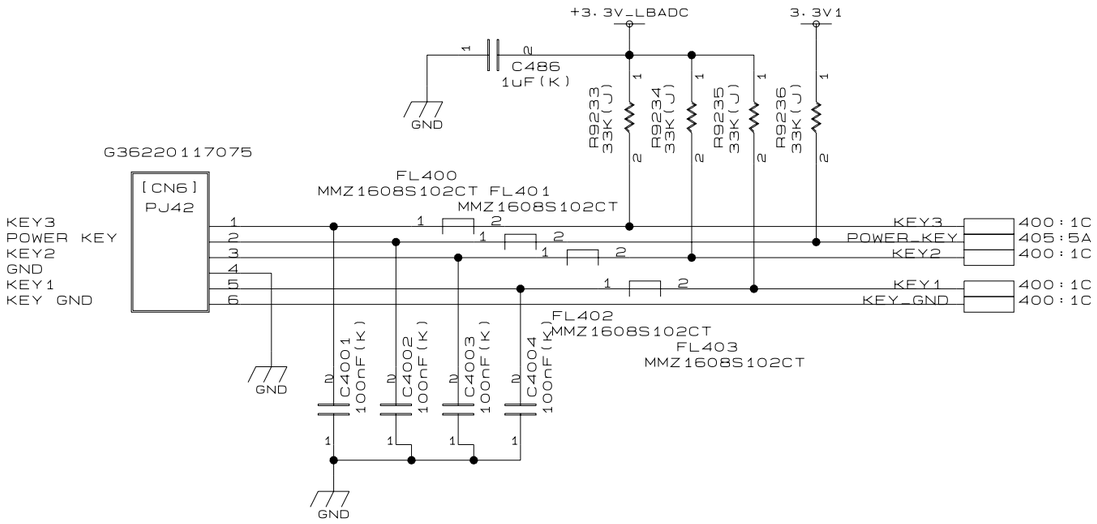
27WL56P
SIGNAL
FLASH MEMORY

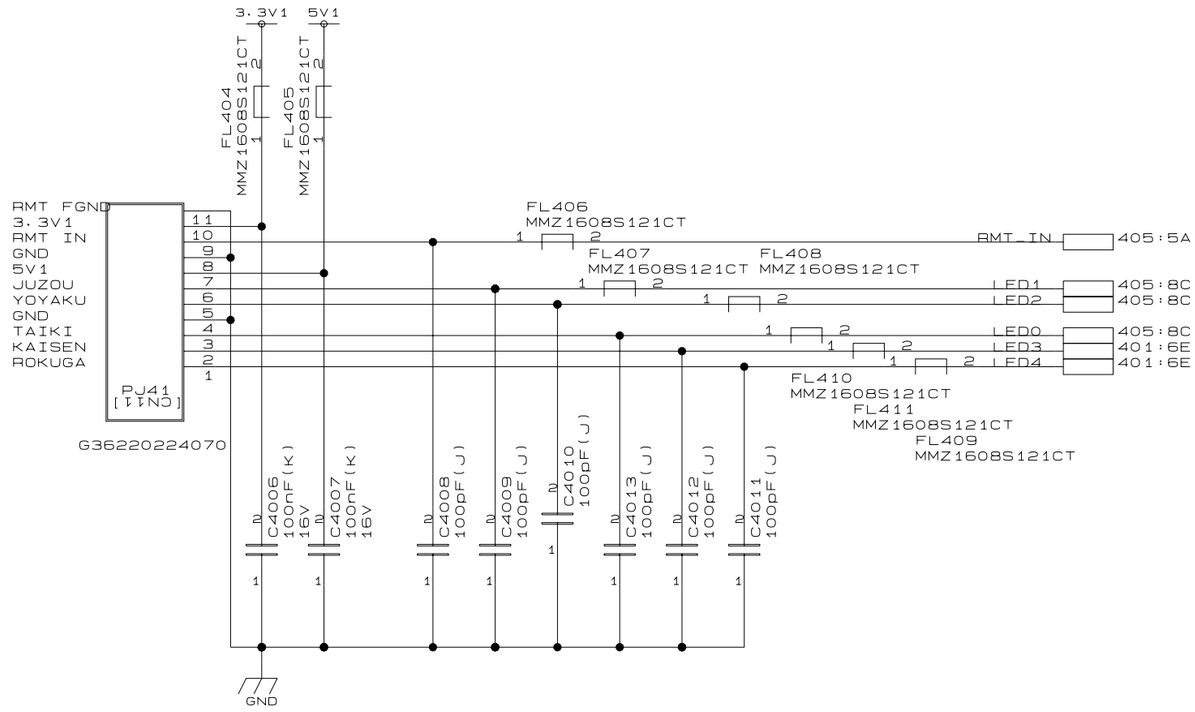
SH. NO.
402

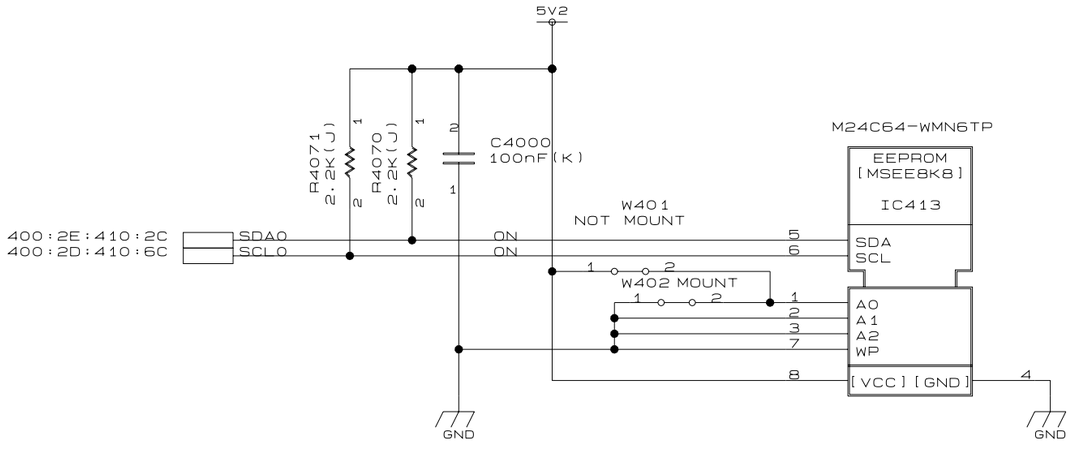


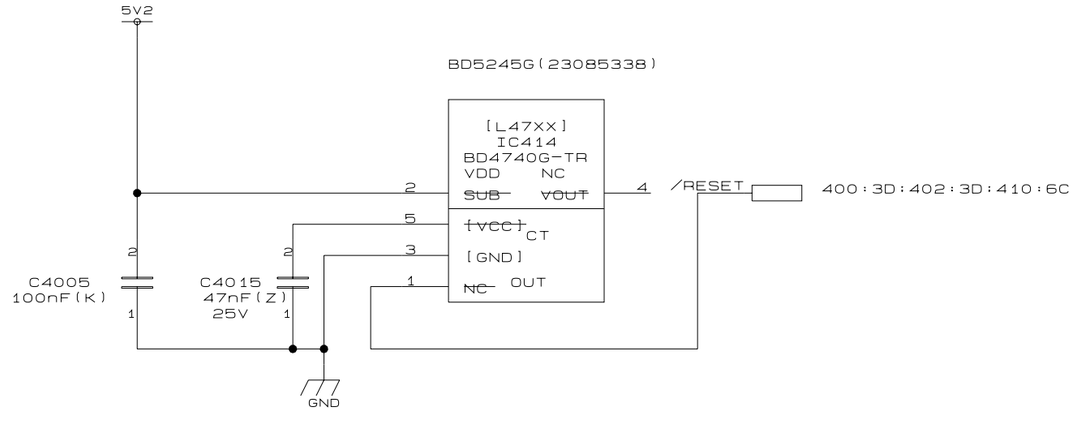
27WL56P
SIGNAL
SYNC SEPA

SH. NO.
403



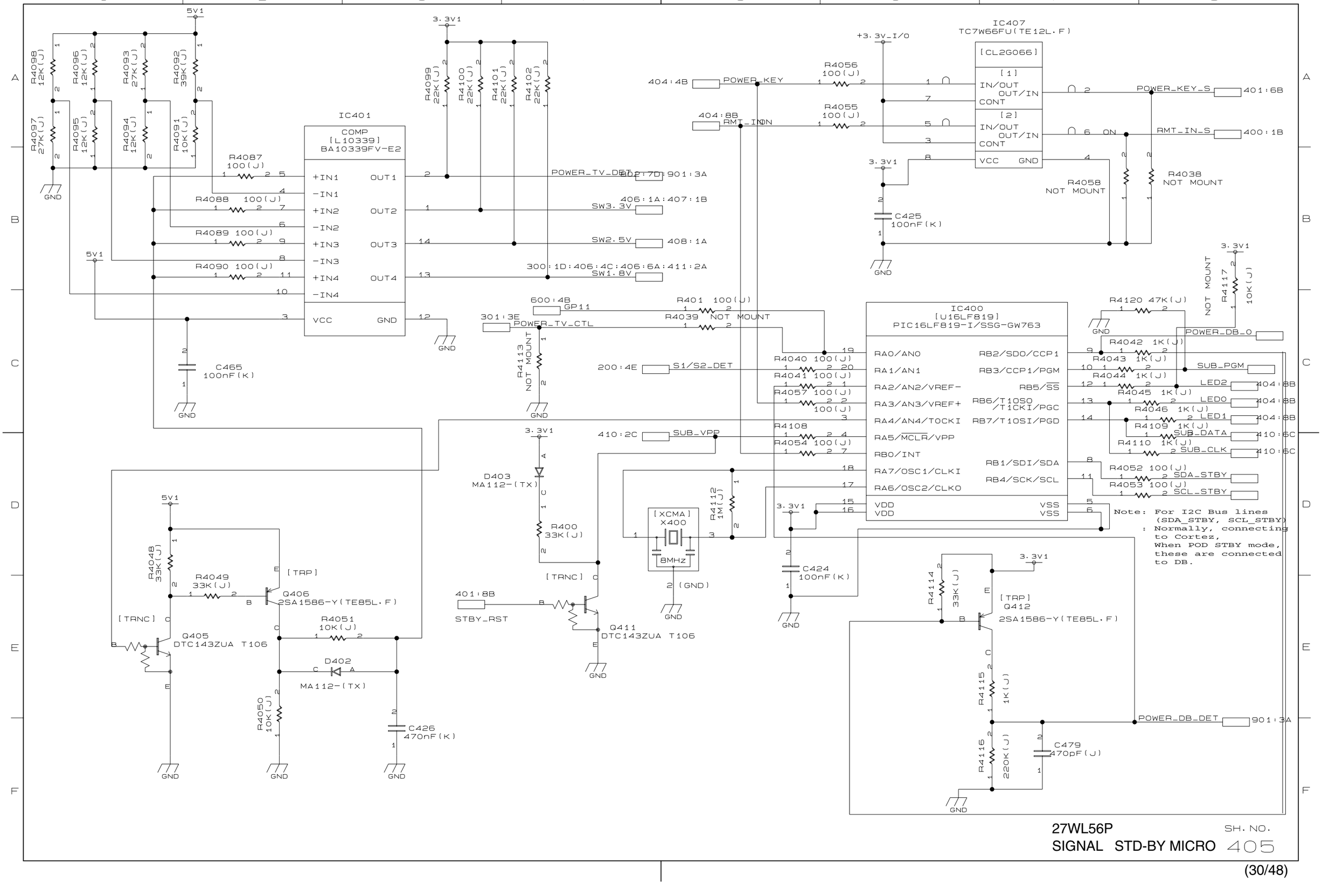






27WL56P
SIGNAL
E2P OTHER

SH. NO.
404



Note: For I2C Bus lines (SDA_STBY, SCL_STBY) : Normally, connecting to Cortez, When POD STBY mode, these are connected to DB.

27WL56P SH. NO.

SIGNAL STD-BY MICRO 405

405:4B:407:1B

SW3.3V
R4059
100(J)
C471
47pF(J)
GND
IC402
PQ070XH02ZPH
UREG
[LPQ070XH]
3.3V
VC
VADJ
VIN VO
[GND]
5V2
C428
1uF(K)
+3.3V
C427
47uF(M)
[CP]
C487
1uF(K)
6.3V
FL423
MPZ2012S221AT
+3.3V_I/O
C451
100nF(K)
16V
1.5K(F)
R4061
150(F)
R4062
R4060
1K(F)

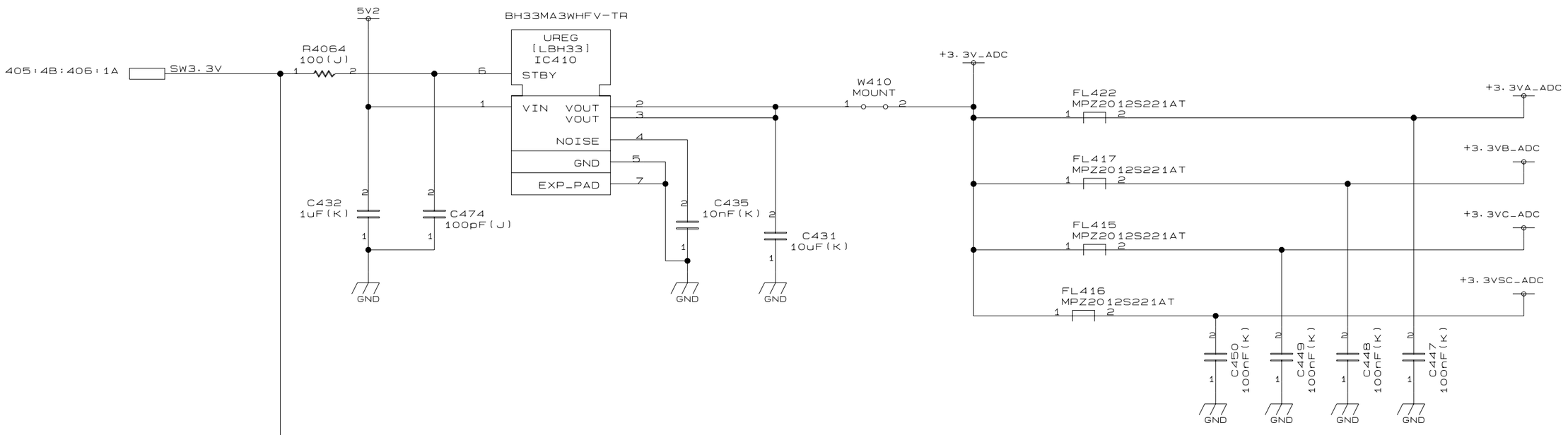
300:1D:405:4B:406:6A:411:2A
SW1.8V
R4063
100(J)
C473
100pF(J)
PQ018EH02ZPH
UREG
[LPQXXXEH]
IC403
1.8V
VC
NC
C429
1uF(K)
C453
147uF(M)

300:1D:405:4B:406:4C:411:2A
SW1.8V
R4072
100(J)
BH18LB1WG-TR
UREG
[LBHXX]
IC412
STBY NC
VIN VOUT
[GND]
C452
100nF(K)
C472
100pF(J)
FL427
MPZ1608S221AT
+1.8V_ADC
C454
1uF(K)

NOT MOUNT
FL428
MPZ1608S221AT
FL425
MPZ1608S221AT
+1.8V_DLL
FL426
MPZ1608S221AT
+1.8V_PLL
FL424
MPZ1608S221AT
+1.8V_CORE
C430
100nF(K)
C456
100nF(K)
C455
100nF(K)

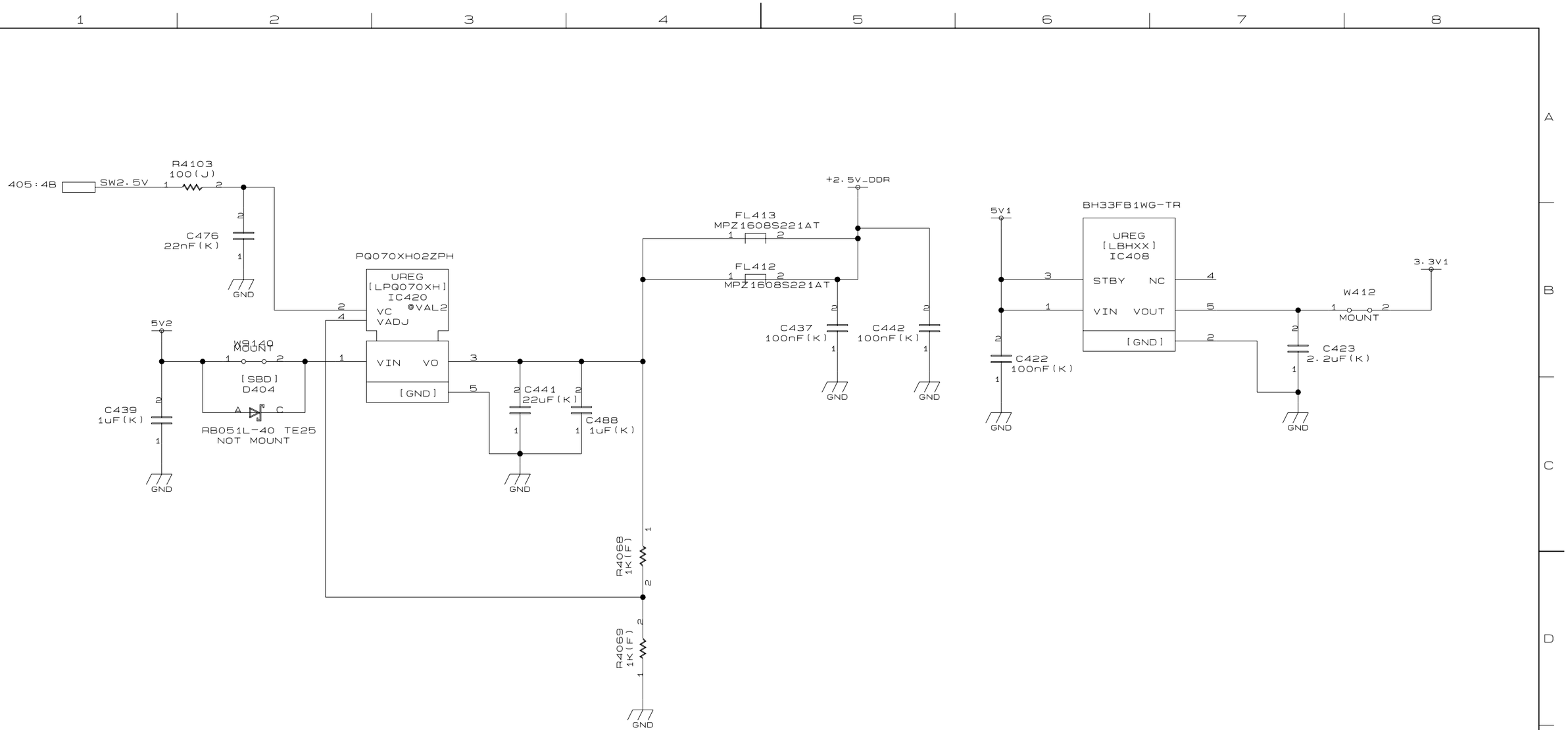
27WL56P
SIGNAL
CORTEZ REG 1

SH. NO.
406



27WL56P
SIGNAL
CORTEZ REG 2

SH. NO.
407

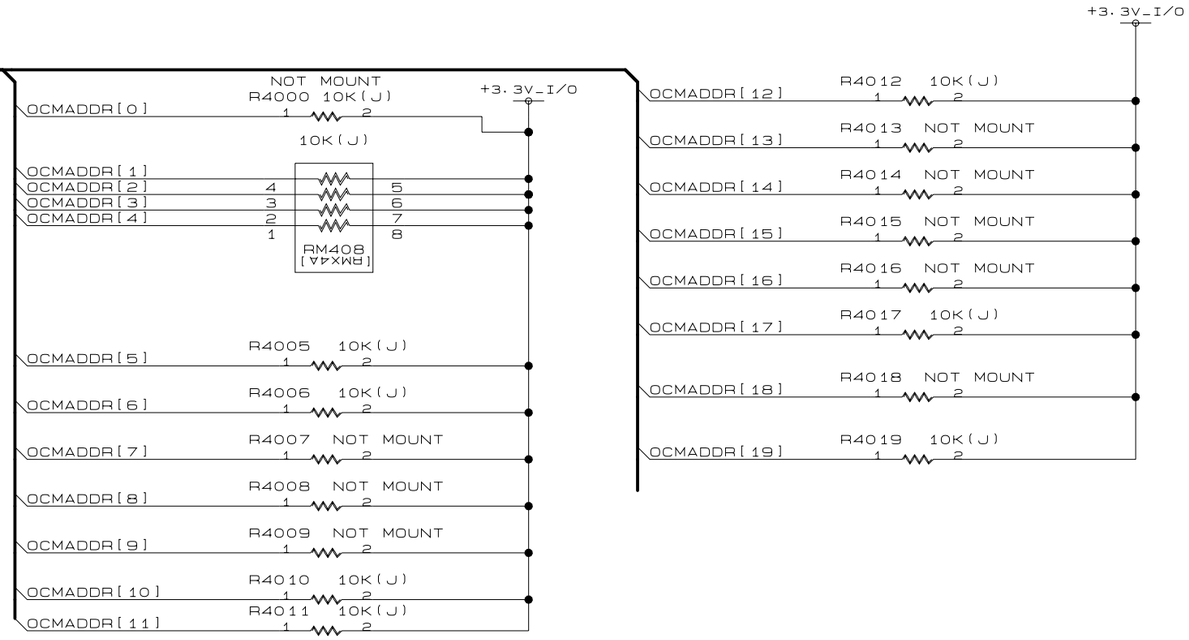


27WL56P
SIGNAL
CORTEZ REG 3

SH. NO.
408

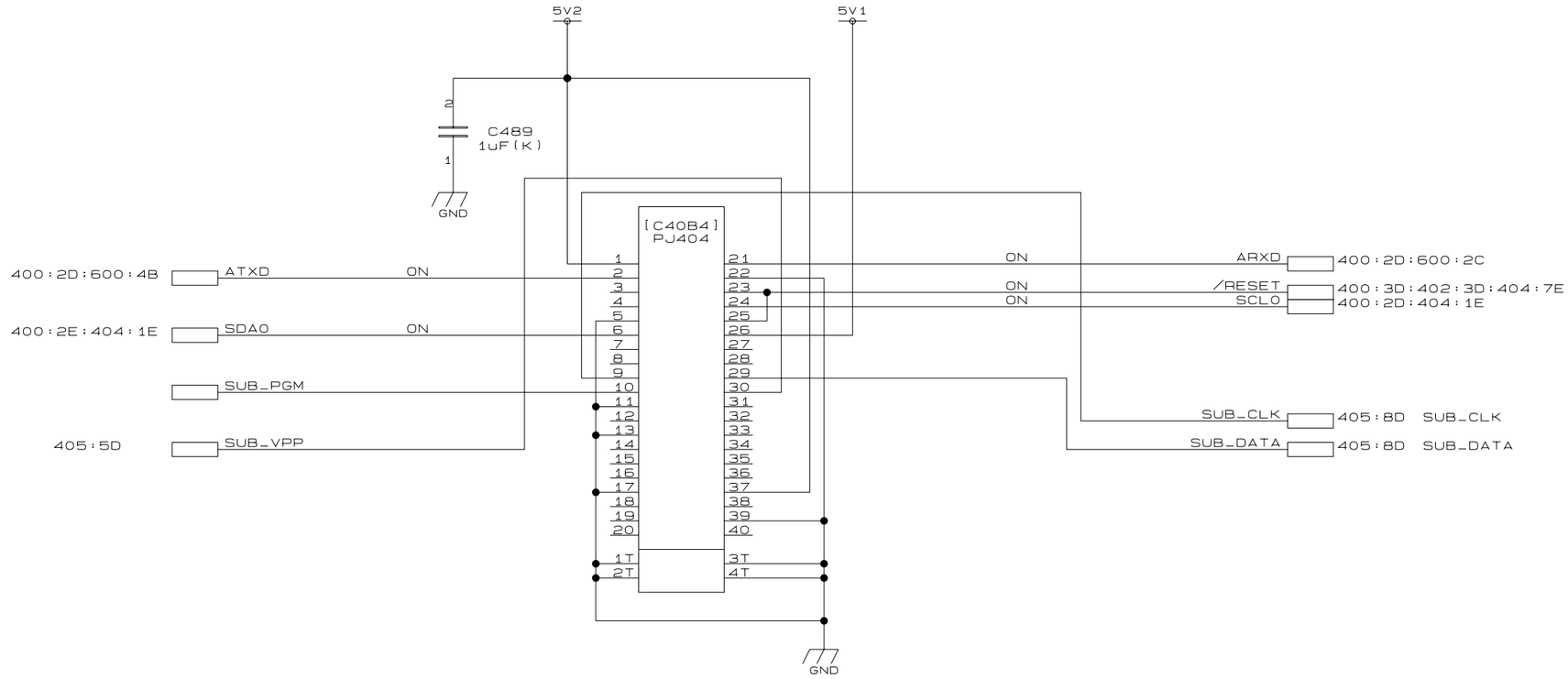
401:1A:402:3A

OCMADDR[0-19]



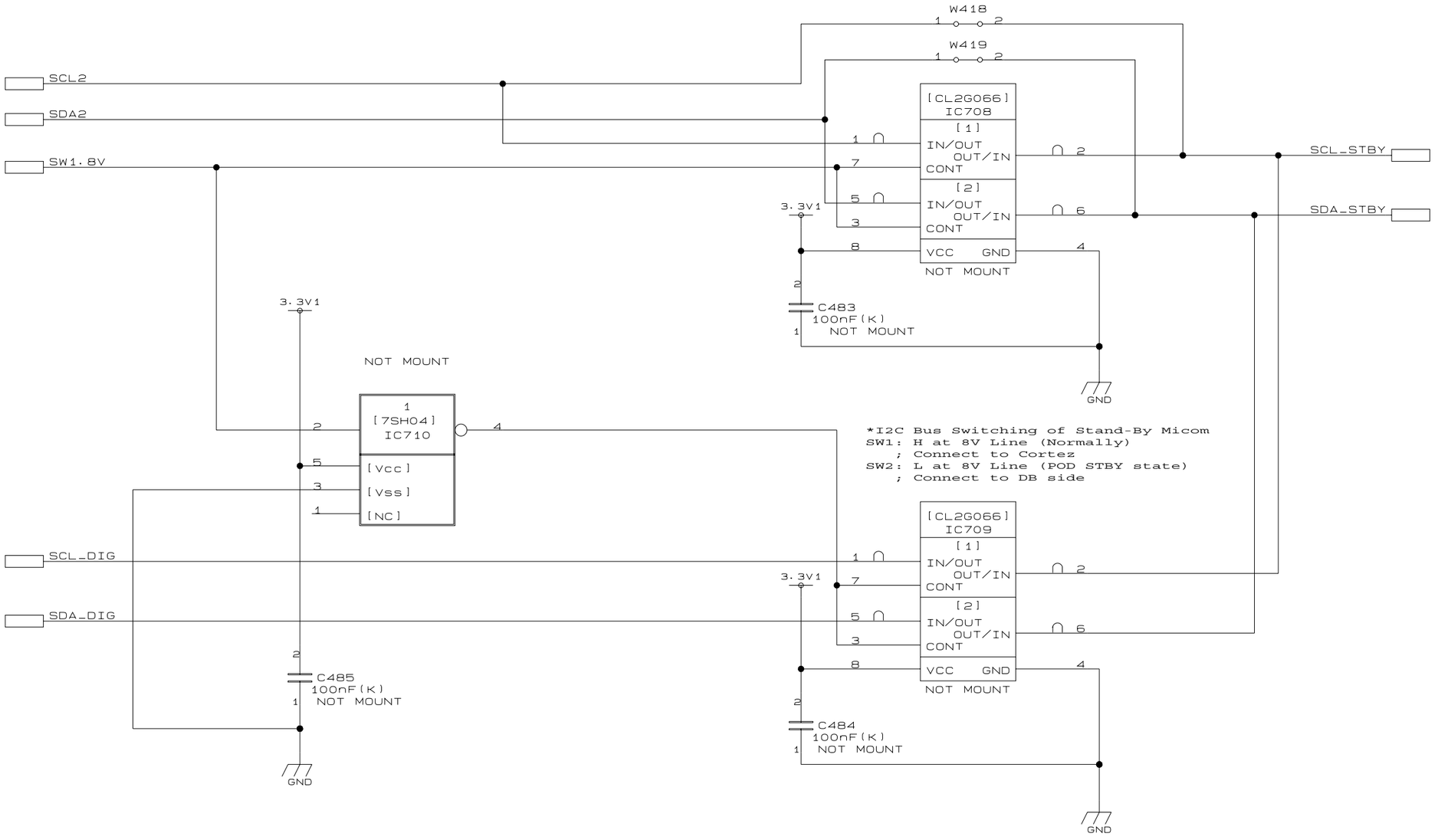
27WL56P
SIGNAL
BOOT CONFIG

SH. NO.
409



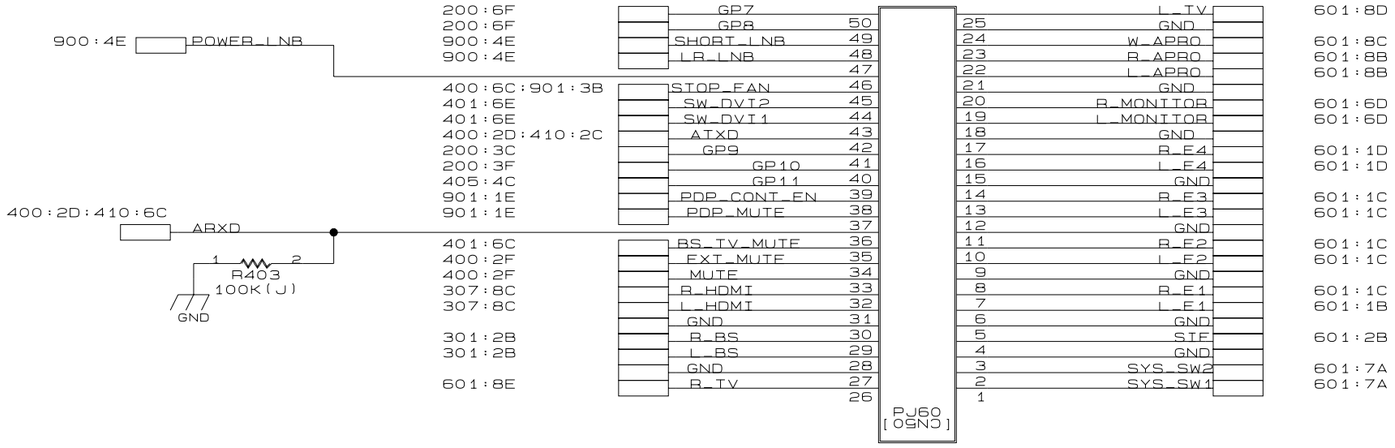
27WL56P
SIGNAL
SERVICE CONNECTOR

SH. NO.
410



27WL56P
SIGNAL
I2C Switch

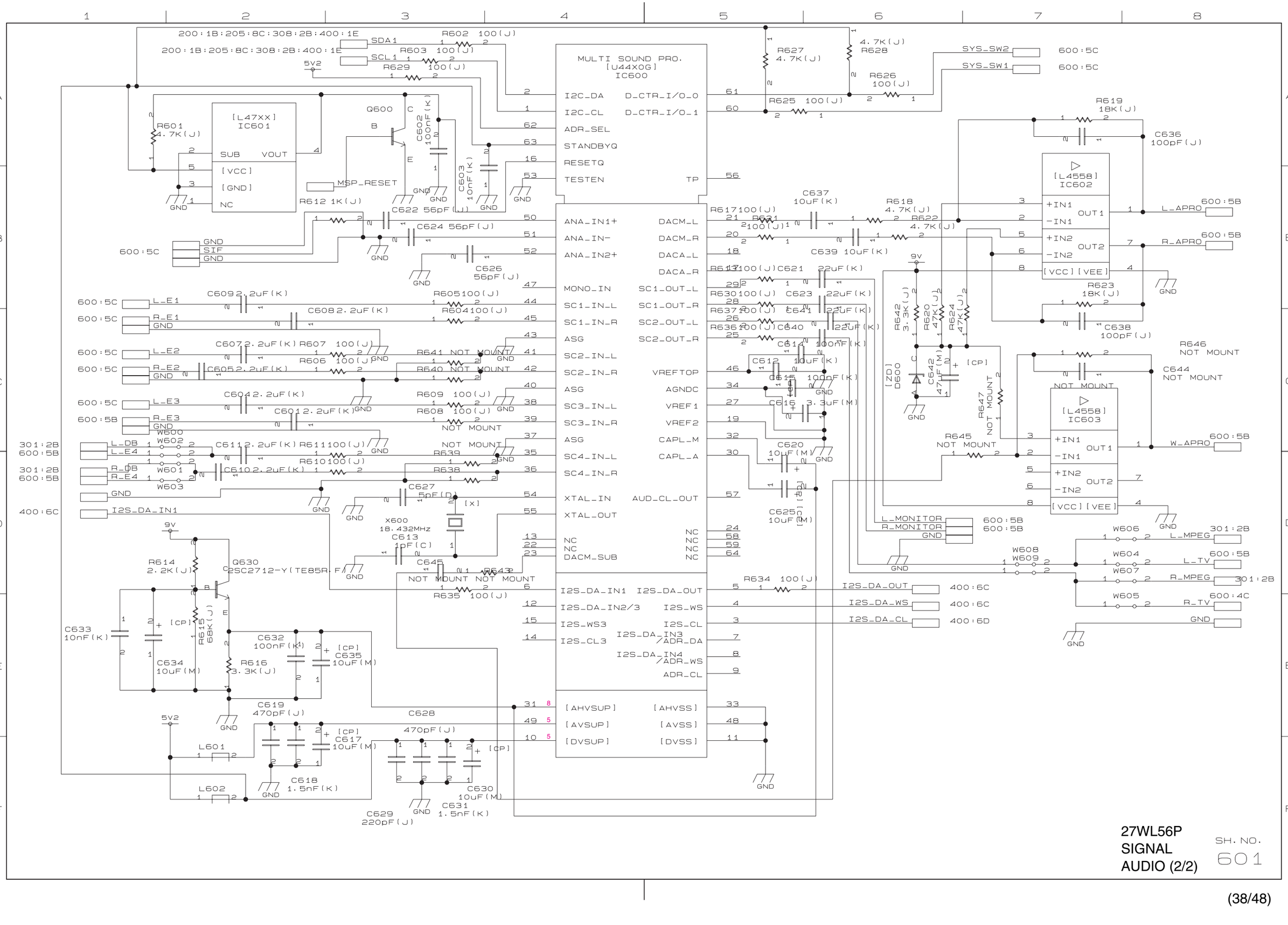
SH. NO.
411



G36220157026

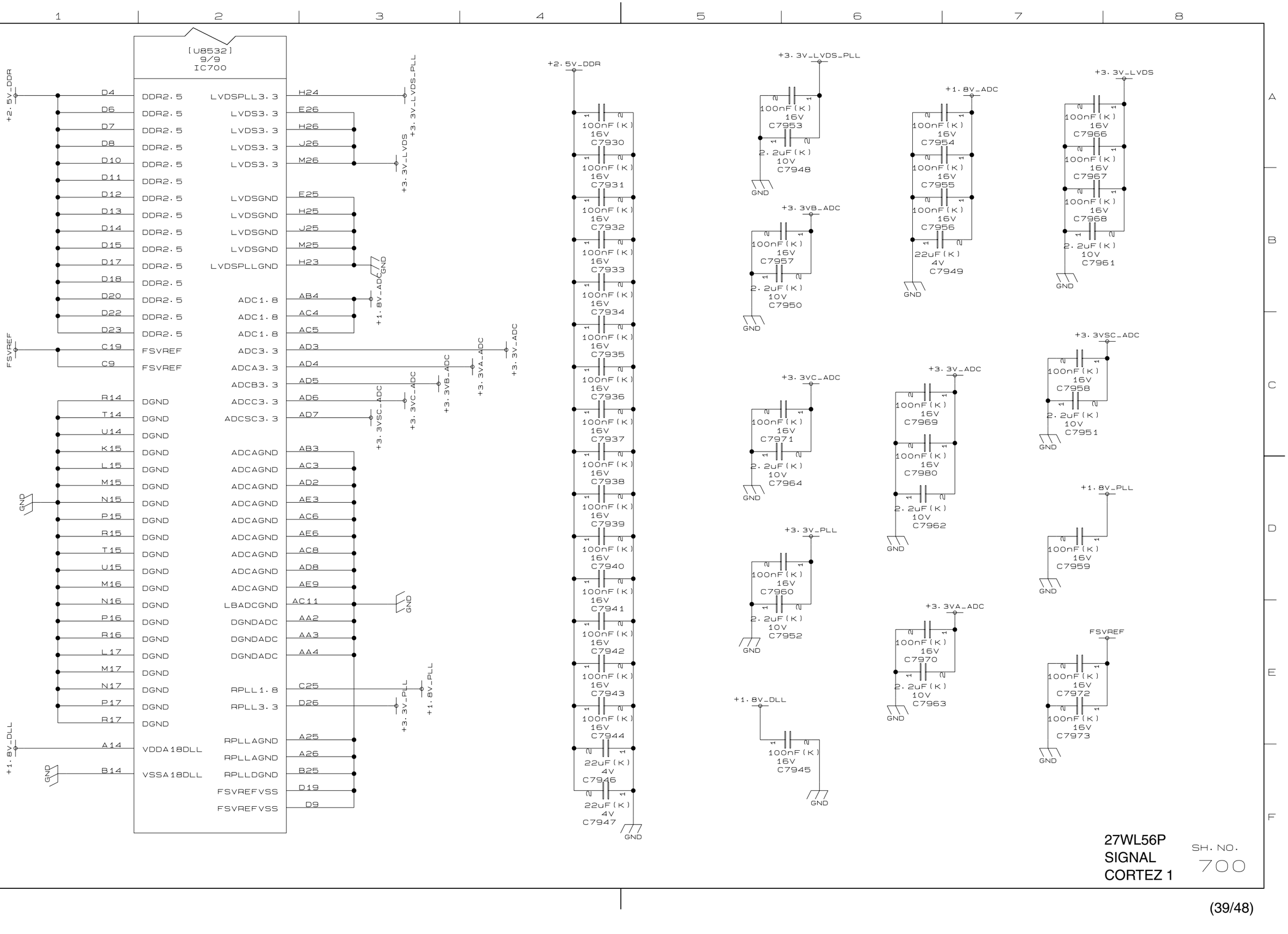
27WL56P
SIGNAL
AUDIO (1/2)

SH. NO.
600



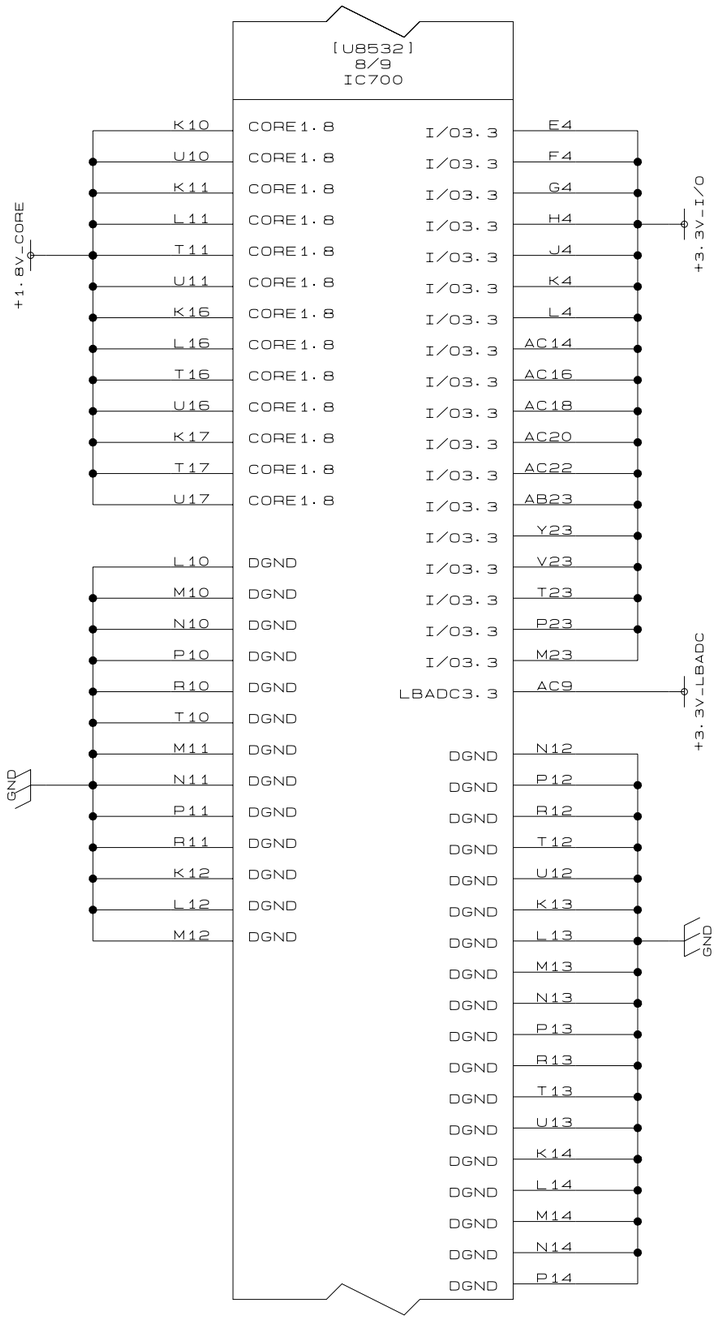
27WL56P
SIGNAL
AUDIO (2/2)

SH. NO.
601



27WL56P
SIGNAL
CORTEZ 1

SH. NO.
700



27WL56P
SIGNAL
CORTEZ 2

SH. NO.
701

/FSCAS. /FSCS[0-1]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDATA[0-31]. FSDQM[0-3]. FSDQS[0-3]

[U8532]
5/9
IC700

FSDATA[15] FSDATA[14] FSDATA[13] FSDATA[12] — RM700 [RMX4A] 33(J)
FSDATA[11] FSDATA[10] FSDATA[9] FSDATA[8] — RM701 [RMX4A] 33(J)
FSDATA[7] FSDATA[6] FSDATA[5] FSDATA[4] — RM702 [RMX4A] 33(J)
FSDATA[3] FSDATA[2] FSDATA[1] FSDATA[0] — RM703 [RMX4A] 33(J)
FSDATA[31] FSDATA[30] FSDATA[29] FSDATA[28] — RM704 [RMX4A] 33(J)
FSDATA[27] FSDATA[26] FSDATA[25] FSDATA[24] — RM705 [RMX4A] 33(J)
FSDATA[23] FSDATA[22] FSDATA[21] FSDATA[20] — RM706 [RMX4A] 33(J)
FSDATA[19] FSDATA[18] FSDATA[17] FSDATA[16] — RM737 [RMX4A] 33(J)

B4	FSDATA0	FSBKSEL1	C20 — R736 — FSBKSEL[1]
A4	FSDATA1	FSBKSEL0	C21 — R735 — FSBKSEL[0]
B5	FSDATA2	FSCKE	C4 — R734 — FSCKE
A5	FSDATA3	FSRAS	C24 — RM707 [RMX4A] 33(J) — /FSRAS
B7	FSDATA4	FSCAS	D24 — RM707 [RMX4A] 33(J) — /FSCAS
A7	FSDATA5	FSWE	C23 — RM707 [RMX4A] 33(J) — /FSWE
B8	FSDATA6		
A8	FSDATA7	FSDQM3	B22 — R740 — FSDQM[2]
B9	FSDATA8	FSDQM2	B17 — R739 — FSDQM[3]
A9	FSDATA9	FSDQM1	A11 — R738 — FSDQM[0]
B10	FSDATA10	FSDQM0	A6 — R737 — FSDQM[1]
A10	FSDATA11	FSDQS3	A22 — R744 — FSDQS[2]
B12	FSDATA12	FSDQS2	A17 — R743 — FSDQS[3]
A12	FSDATA13	FSDQS1	B11 — R742 — FSDQS[0]
B13	FSDATA14	FSDQS0	B6 — R741 — FSDQS[1]
A13	FSDATA15	FSCS1	C22 — R716 — /FSCS[1]
B15	FSDATA16	FSCS0	D21 — R717 — /FSCS[0]
A15	FSDATA17	FSCLKn	C5 — R718 — FSCLK-
B16	FSDATA18	FSCLKp	D5 — R719 — FSCLK+
A16	FSDATA19		
B18	FSDATA20	FSADDR12	C11 — RM708 [RMX4A] 33(J) — FSADDR[11]
A18	FSDATA21	FSADDR11	C14 — RM708 [RMX4A] 33(J) — FSADDR[4]
B19	FSDATA22	FSADDR10	D16 — RM708 [RMX4A] 33(J) — FSADDR[9]
A19	FSDATA23	FSADDR9	C12 — RM708 [RMX4A] 33(J) — FSADDR[12]
B20	FSDATA24	FSADDR8	C6 — R707 33(J) — FSADDR[10]
A20	FSDATA25	FSADDR7	C7 — RM709 [RMX4A] 33(J) — FSADDR[8]
B21	FSDATA26	FSADDR6	C8 — RM709 [RMX4A] 33(J) — FSADDR[7]
A21	FSDATA27	FSADDR5	C10 — RM709 [RMX4A] 33(J) — FSADDR[6]
B23	FSDATA28	FSADDR4	C13 — RM709 [RMX4A] 33(J) — FSADDR[5]
A23	FSDATA29	FSADDR3	C15 — FSADDR[3]
B24	FSDATA30	FSADDR2	C16 — FSADDR[2]
A24	FSDATA31	FSADDR1	C17 — RM710 [RMX4A] 33(J) — FSADDR[1]
		FSADDR0	C18 — RM710 [RMX4A] 33(J) — FSADDR[0]

27WL56P
SIGNAL
DDR I/F

SH. NO.
702

/FSCAS. /FSCS[0]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDQM[0-1]. FSDQS[0-1]

/FSCAS. /FSCS[0]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDATA[0-15]. FSDQM[2-3]. FSDQS[2-3]

702:2A:704:3A

FSDATA[16-31]

HY5DU281622ETP-5 [MDSD8M16] IC701

HY5DU281622ETP-5 [MDSD8M16] IC702

FSADDR[0] 29 A0
FSADDR[1] 30 A1
FSADDR[2] 31 A2
FSADDR[3] 32 A3
FSADDR[4] 35 A4
FSADDR[5] 36 A5
FSADDR[6] 37 A6
FSADDR[7] 38 A7
FSADDR[8] 39 A8
FSADDR[9] 40 A9
FSADDR[10] 28 A10/AP
FSADDR[11] 41 A11

FSBKSEL[1] 27 BS1
FSBKSEL[0] 26 BS0
FSCLK- 46 CLK
FSCLK+ 45 CLK
FSCKE 44 CKE

R732 220(J) NOT MOUNT
R733 220(J) NOT MOUNT

/FSCS[0] 24 CS
/FSRAS 23 RAS
/FSCAS 22 CAS
/FSWE 21 WE

R720 33(J) FSDQM[0] 20 LDM
R721 33(J) FSDQM[1] 47 UDM
R722 33(J) FSDQS[0] 16 LDQS
R723 33(J) FSDQS[1] 51 UDQS

R724 33(J) FSDQM[2] 20 LDM
R725 33(J) FSDQM[3] 47 UDM
R726 33(J) FSDQS[2] 16 LDQS
R727 33(J) FSDQS[3] 51 UDQS

14 NC
17 NC
19 NC
25 NC
FSADDR[12] 42 NC
43 NC
50 NC
53 NC

DQ0 2 1 [RMX4A] RM711 33(J) 8 FSDATA[0]
DQ1 4 2 7 FSDATA[1]
DQ2 5 3 6 FSDATA[2]
DQ3 7 4 5 FSDATA[3]
DQ4 8 1 [RMX4A] RM712 33(J) 8 FSDATA[4]
DQ5 10 2 7 FSDATA[5]
DQ6 11 3 6 FSDATA[6]
DQ7 13 4 5 FSDATA[7]
DQ8 54 1 [RMX4A] RM713 33(J) 8 FSDATA[8]
DQ9 56 2 7 FSDATA[9]
DQ10 57 3 6 FSDATA[10]
DQ11 59 4 5 FSDATA[11]
DQ12 60 1 [RMX4A] RM714 33(J) 8 FSDATA[12]
DQ13 62 2 7 FSDATA[13]
DQ14 63 3 6 FSDATA[14]
DQ15 65 4 5 FSDATA[15]

DQ0 2 1 [RMX4A] RM715 33(J) 8 FSDATA[16]
DQ1 4 2 7 FSDATA[17]
DQ2 5 3 6 FSDATA[18]
DQ3 7 4 5 FSDATA[19]
DQ4 8 1 [RMX4A] RM716 33(J) 8 FSDATA[20]
DQ5 10 2 7 FSDATA[21]
DQ6 11 3 6 FSDATA[22]
DQ7 13 4 5 FSDATA[23]
DQ8 54 1 [RMX4A] RM717 33(J) 8 FSDATA[24]
DQ9 56 2 7 FSDATA[25]
DQ10 57 3 6 FSDATA[26]
DQ11 59 4 5 FSDATA[27]
DQ12 60 1 [RMX4A] RM718 33(J) 8 FSDATA[28]
DQ13 62 2 7 FSDATA[29]
DQ14 63 3 6 FSDATA[30]
DQ15 65 4 5 FSDATA[31]

[VDDQ] 3 6 [VSSQ]
[VDDQ] 9 12 [VSSQ]
[VDDQ] 15 52 [VSSQ]
[VDDQ] 55 58 [VSSQ]
[VDDQ] 61 64 [VSSQ]
[VDD] 1 34 [VSS]
[VDD] 18 48 [VSS]
[VDD] 33 66 [VSS]
[VREF] 49

+2.5V_DDR
FSVREF

C774 100nF(K)
C775 100nF(K)
C776 100nF(K)
C777 100nF(K)
C778 100nF(K)
C779 100nF(K)
C780 100nF(K)

C781 100nF(K)
C782 100nF(K)
C783 100nF(K)
C784 100nF(K)
C785 100nF(K)
C786 100nF(K)
C787 100nF(K)

GND

27WL56P
SIGNAL
DDR SDRAM

SH. NO. 703

/FSCAS. /FSCS[0-1]. /FSRAS. /FSWE. FSADDR[0-12]. FSBKSEL[0-1]. FSCKE. FSCLK+. FSCLK-. FSDATA[0-31]. FSDQM[0-3]. FSDQS[0-3]

+2.5V_DDR

R9220 10K(F) MOUNT

REG. [L2995] IC703 LP2995MX

VDDQ VREF VSENSE NC VTT [AVIN] [PVIN] [GND]

W9116

FSVREF

GND

C788 1uF(K) 10V

C789 220uF(M) [CP] 16V

C790 10nF(K)

C773 22uF(K) 4V MOUNT

R9221 10K(F) MOUNT

C791 100nF(K)

C792 100nF(K) C793 100nF(K) C795 100nF(K) C797 100nF(K) C799 100nF(K) C7001 100nF(K) C7003 100nF(K) C7004 100nF(K) C7005 100nF(K) C7006 100nF(K)

RM719 RM720 RM721 RM723 RM725 RM727 RM729 RM730 RM731 RM732 RM733 [RMX4A] 150(J)

FSDATA[31] FSDATA[30] FSDATA[29] FSDATA[28] FSDATA[27] FSDATA[26] FSDATA[25] FSDATA[24] FSDATA[23] FSDATA[22] FSDATA[21] FSDATA[20] FSDATA[19] FSDATA[18] FSDATA[17] FSDATA[16] FSDATA[15] FSDATA[14] FSDATA[13] FSDATA[12] FSDATA[11] FSDATA[10] FSDATA[9] FSDATA[8] FSDATA[7] FSDATA[6] FSDATA[5] FSDATA[4] FSDATA[3] FSDATA[2] FSDATA[1] FSDATA[0] FSADDR[10] FSADDR[11] FSADDR[4] FSADDR[9] FSADDR[12] FSADDR[5] FSADDR[6] FSADDR[7] FSADDR[8] FSADDR[0] FSADDR[1] FSADDR[2] FSADDR[3]

C794 100nF(K) C796 100nF(K) C798 100nF(K) C7000 100nF(K) C7002 100nF(K)

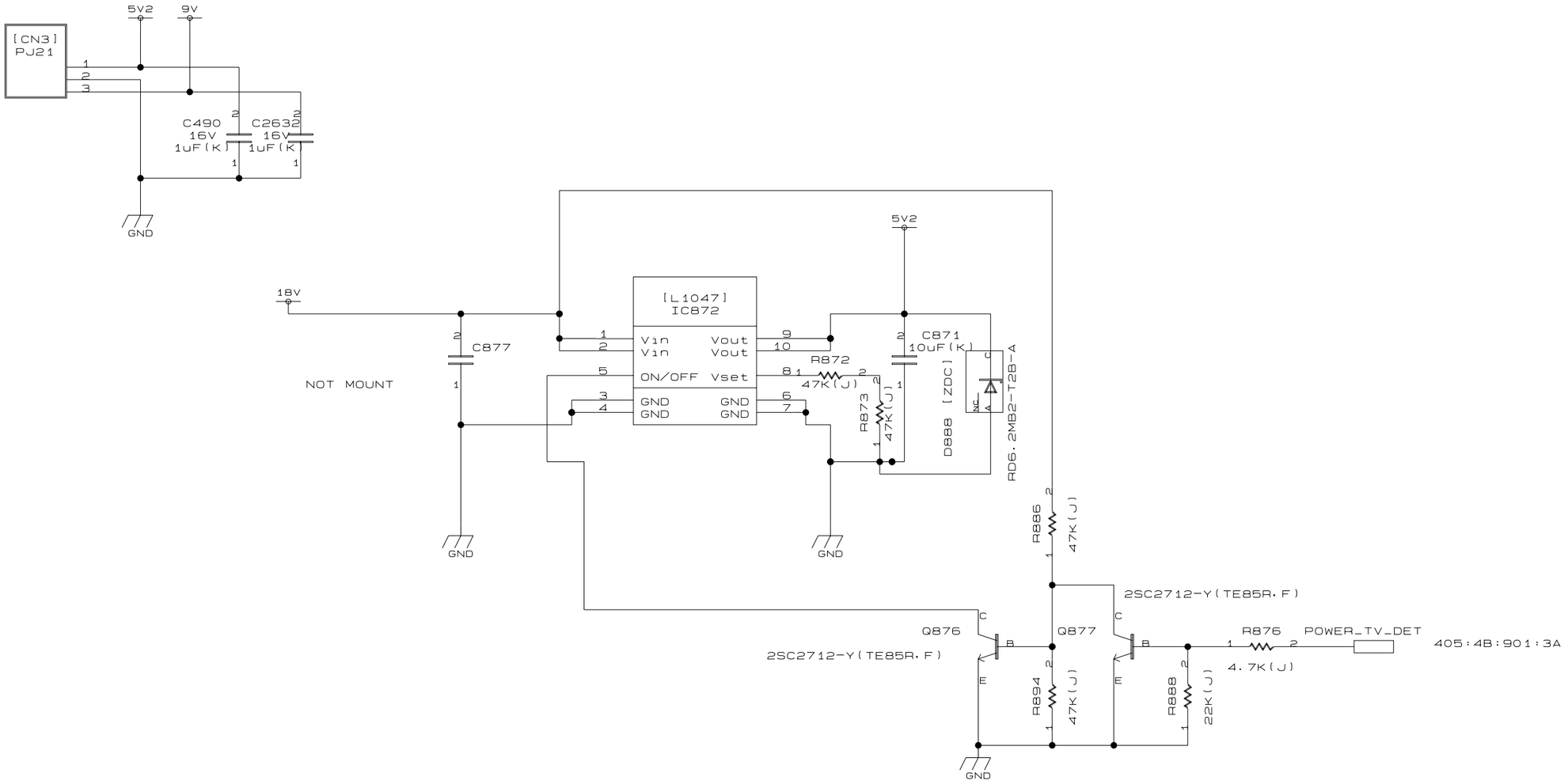
RM722 RM724 RM726 RM728 [RMX4A] 150(J)

/FSBAS /FSCAS /FSWE /FSCS[1] /FSCS[0] FSBKSEL[0] FSBKSEL[1] FSDQM[1] FSDQM[0] FSDQS[0] FSDQS[1] FSDQM[3] FSDQM[2] FSDQS[2] FSDQS[3] FSCLK- FSCLK+ FSCKE

R728 150(J) R729 150(J) R730 150(J)

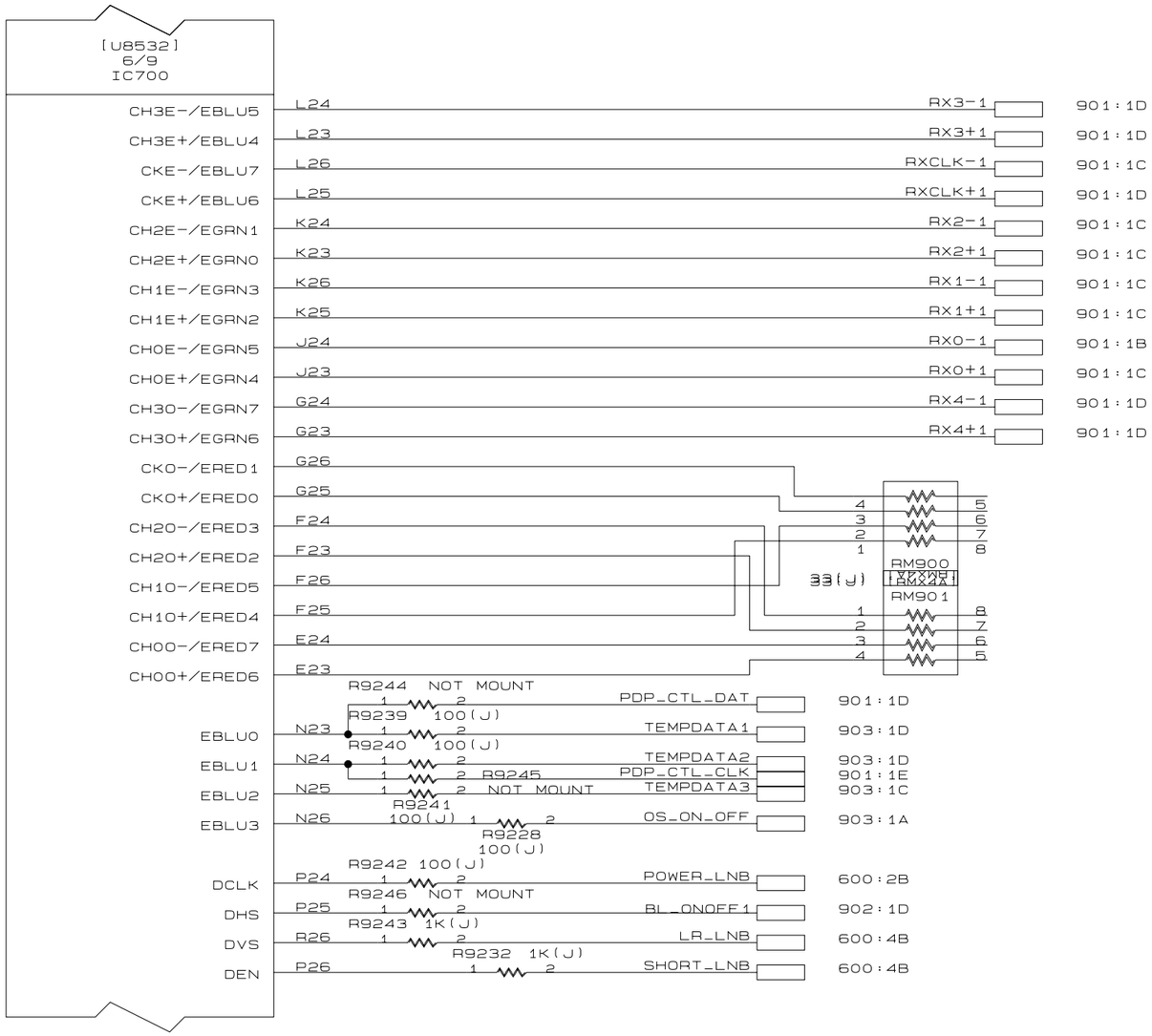
27WL56P
SIGNAL
DDR TERMINATION

SH. NO. 704



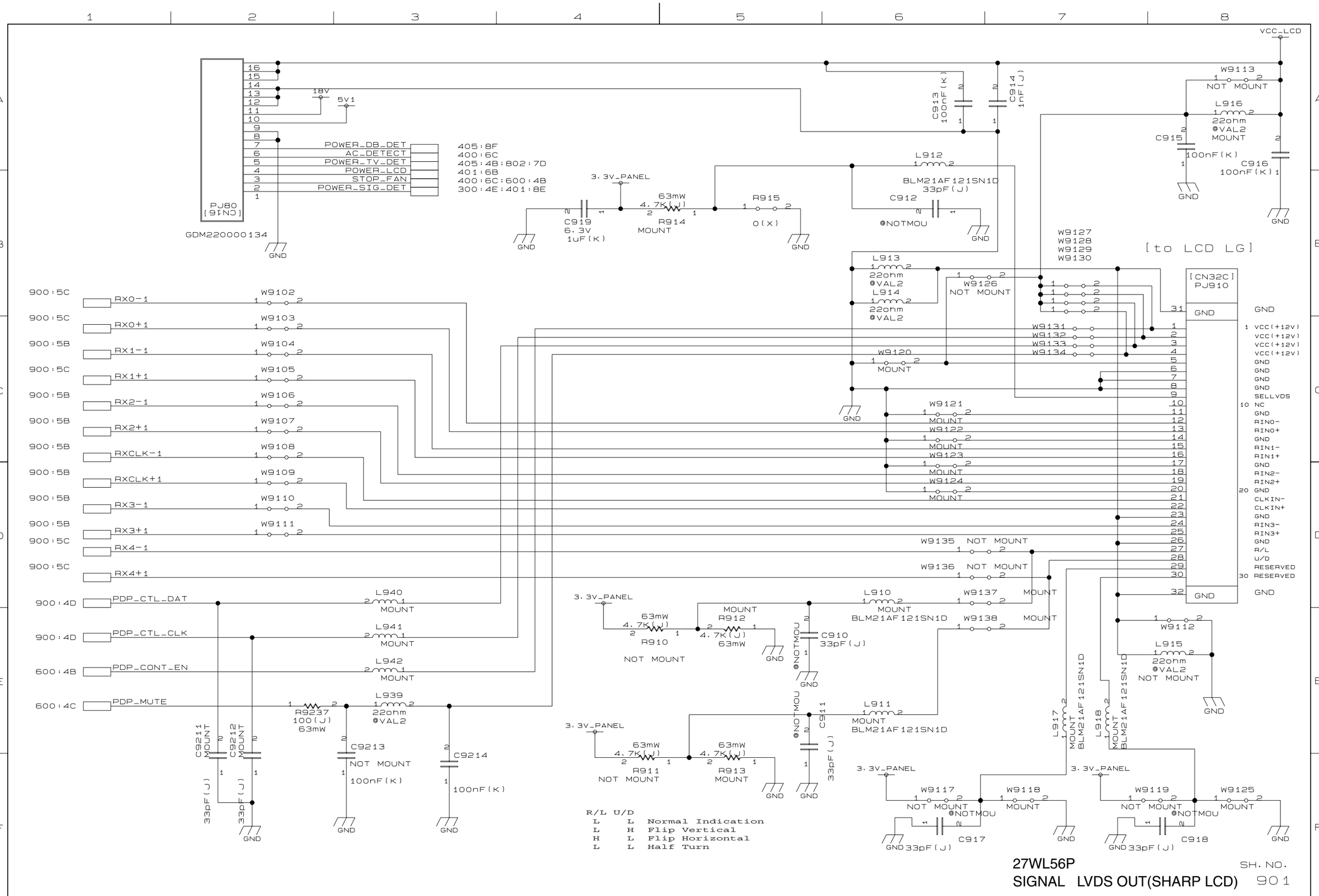
27WL56P
SIGNAL
DCDC CONV.

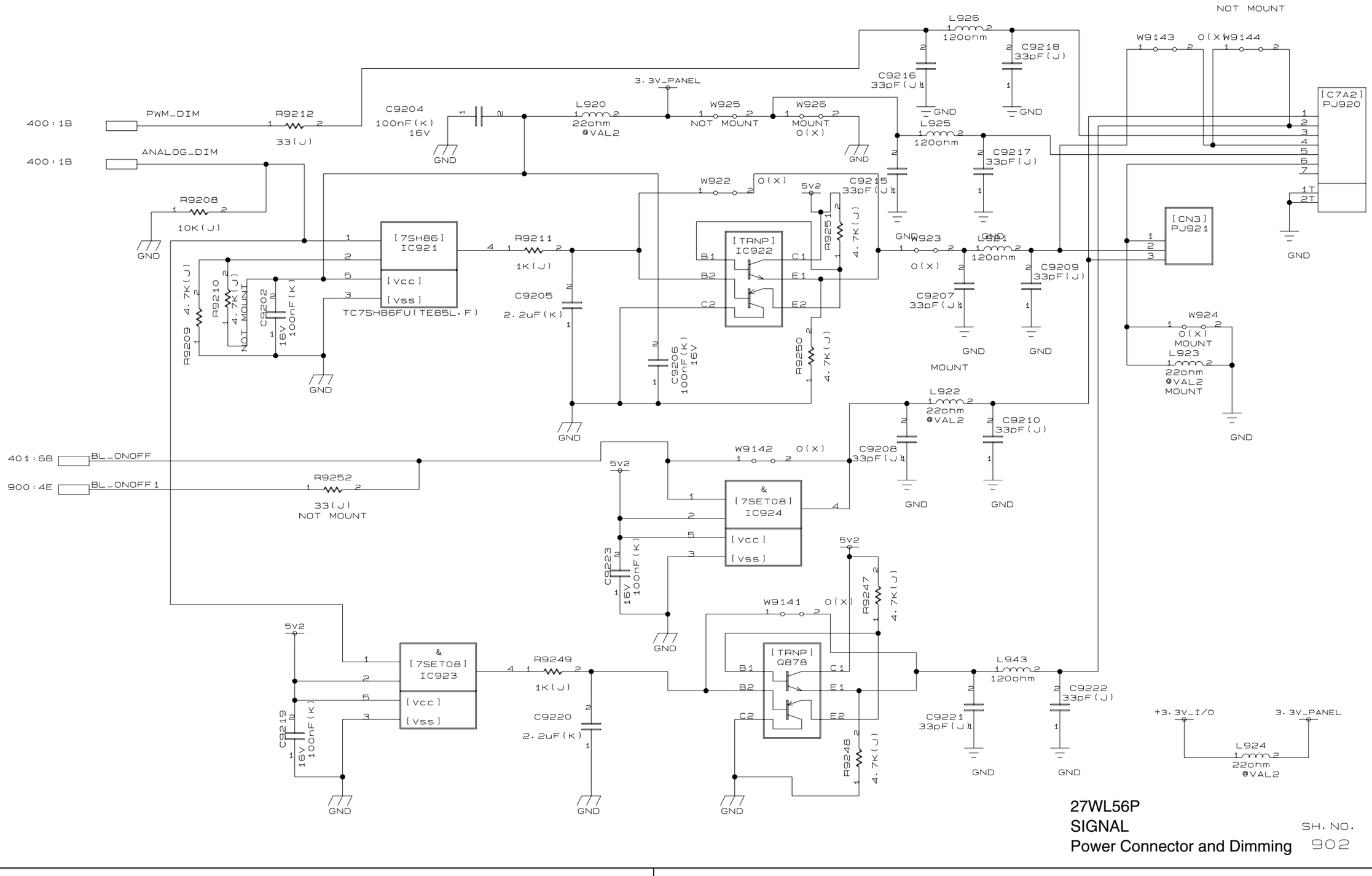
SH. NO.
802

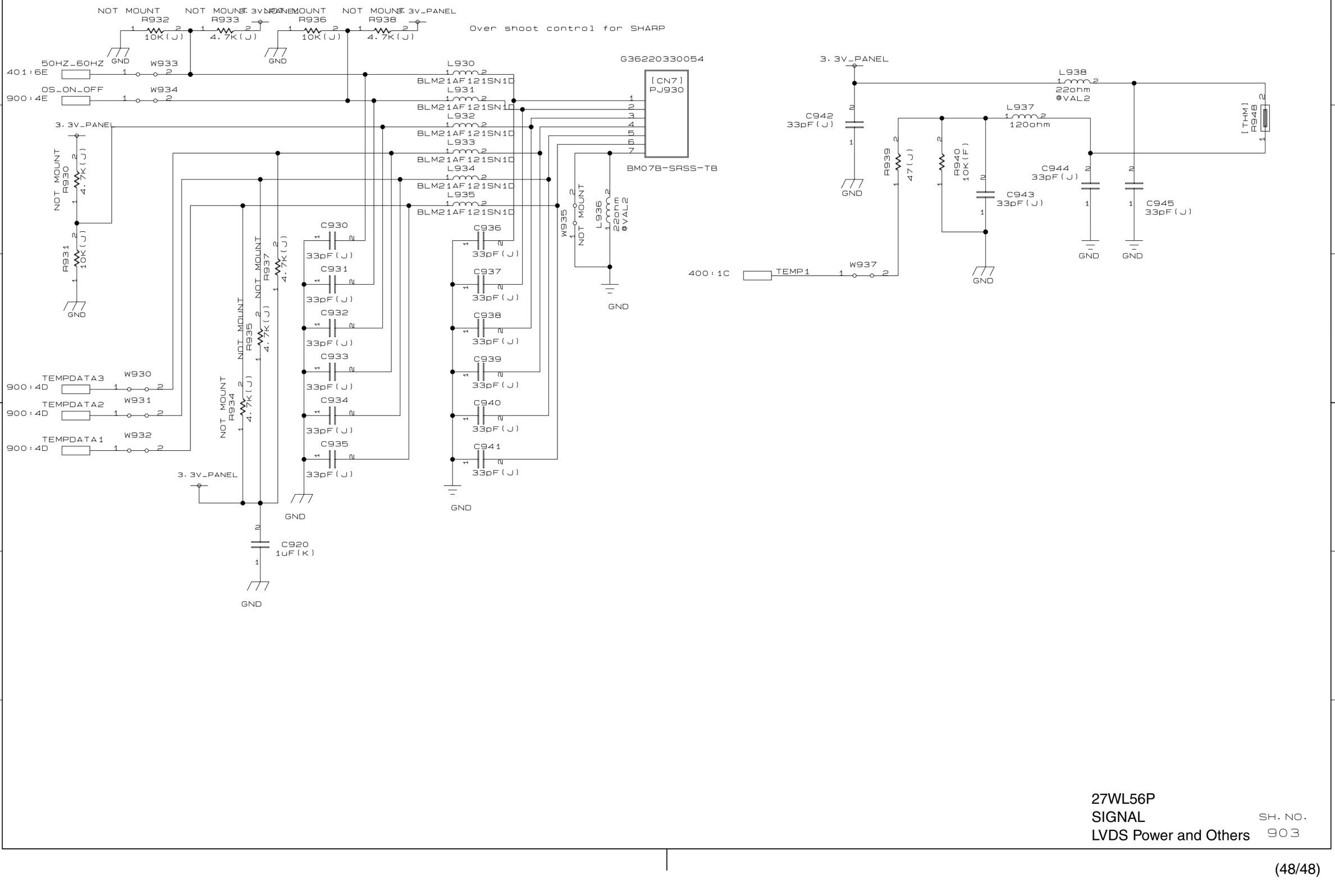


27WL56P
SIGNAL
LVDS OUT

SH. NO.
900


PJ80 [CN16]
GDM220000134
POWER_DB_DET
AC_DETECT
POWER_TV_DET
POWER_LCD
STOP_FAN
POWER_SIG_DET
405:8F
400:6C
405:4B:802:7D
401:6B
400:6C:600:4B
300:4E:401:8E
18V
5V1
3.3V_PANEL
C919 6.3V 1uF(K)
R914 MOUNT
63mW 4.7K(J)
R915 0(X)
C913 100nF(K)
C914 1nF(J)
L912 BLM21AF121SN1D
C912 @NOTMOU 33pF(J)
VCC_LCD
W9113 NOT MOUNT
L916 22ohm @VAL2 MOUNT
C915 100nF(K)
C916 100nF(K)
W9127 W9128 W9129 W9130
[to LCD LG]
L913 22ohm @VAL2
L914 22ohm @VAL2
W9126 NOT MOUNT
W9120 MOUNT
W9121 MOUNT
W9122 MOUNT
W9123 MOUNT
W9124 MOUNT
W9131 W9132 W9133 W9134
[CN32C] PJ910
GND
VCC(+12V)
SELLVDS
NC
RIN0- RIN0+ RIN1- RIN1+ RIN2- RIN2+
CLKIN- CLKIN+
RIN3- RIN3+
R/L
U/D
RESERVED
900:5C RX0-1 W9102
RX0+1 W9103
RX1-1 W9104
RX1+1 W9105
RX2-1 W9106
RX2+1 W9107
RXCLK-1 W9108
RXCLK+1 W9109
RX3-1 W9110
RX3+1 W9111
RX4-1
RX4+1
900:4D PDP_CTL_DAT L940 MOUNT
PDP_CTL_CLK L941 MOUNT
600:4B PDP_CONT_EN L942 MOUNT
600:4C PDP_MUTE R9237 100(J) 63mW L939 22ohm @VAL2
C9211 MOUNT 33pF(J)
C9212 MOUNT 33pF(J)
C9213 NOT MOUNT 100nF(K)
C9214 100nF(K)
W9135 NOT MOUNT
W9136 NOT MOUNT
W9137 MOUNT
W9138 MOUNT
L910 MOUNT BLM21AF121SN1D
L911 MOUNT BLM21AF121SN1D
R910 NOT MOUNT
R912 MOUNT 4.7K(J) 63mW
C910 @NOTMOU 33pF(J)
R911 NOT MOUNT 4.7K(J) 63mW
R913 MOUNT 4.7K(J) 63mW
C911 @NOTMOU 33pF(J)
W9112
L915 22ohm @VAL2 NOT MOUNT
L917 MOUNT BLM21AF121SN1D
L918 MOUNT BLM21AF121SN1D
W9117 NOT MOUNT
W9118 MOUNT
C917 @NOTMOU 33pF(J)
W9119 NOT MOUNT
W9125 MOUNT
C918 @NOTMOU 33pF(J)
R/L U/D
L L Normal Indication
L H Flip Vertical
H L Flip Horizontal
L L Half Turn
27WL56P
SIGNAL LVDS OUT(SHARP LCD)
SH. NO. 901


NOT MOUNT
L926
120ohm
C9216
33pF(J)
C9218
33pF(J)
W9143
O(X)
W9144
3.3V_PANEL
400:1B
PWM_DIM
R9212
33(J)
C9204
100nF(K)
16V
L920
22ohm
@VAL2
W925
NOT MOUNT
W926
MOUNT
O(X)
L925
120ohm
C9217
33pF(J)
[C7A2]
PJ920
400:1B
ANALOG_DIM
R9208
10K(J)
W922
O(X)
5V2
C9215
33pF(J)
[CN3]
PJ921
[7SH86]
IC921
[Vcc]
[Vss]
TC7SH86FU(TE85L.F)
R9211
1K(J)
C9205
2.2uF(K)
[TRNP]
IC922
R9251
4.7K(J)
GND
W923
O(X)
L921
120ohm
C9209
33pF(J)
C9207
33pF(J)
R9209 4.7K(J)
R9210
4.7K(J)
NOT MOUNT
C9202
16V
100nF(K)
C9206
100nF(K)
16V
R9250
4.7K(J)
W924
O(X)
MOUNT
L923
220hm
@VAL2
MOUNT
L922
22ohm
@VAL2
C9210
33pF(J)
C9208
33pF(J)
401:6B
BL_ONOFF
900:4E
BL_ONOFF1
R9252
33(J)
NOT MOUNT
W9142
O(X)
&
[7SET08]
IC924
C9223
16V
100nF(K)
W9141
O(X)
R9247
4.7K(J)
&
[7SET08]
IC923
C9219
16V
100nF(K)
R9249
1K(J)
C9220
2.2uF(K)
[TRNP]
Q878
L943
120ohm
C9222
33pF(J)
C9221
33pF(J)
R9248
4.7K(J)
+3.3V_I/O
3.3V_PANEL
L924
22ohm
@VAL2
27WL56P
SIGNAL
Power Connector and Dimming
SH. NO.
902



27WL56P
SIGNAL
LVDS Power and Others

SH. NO.
903